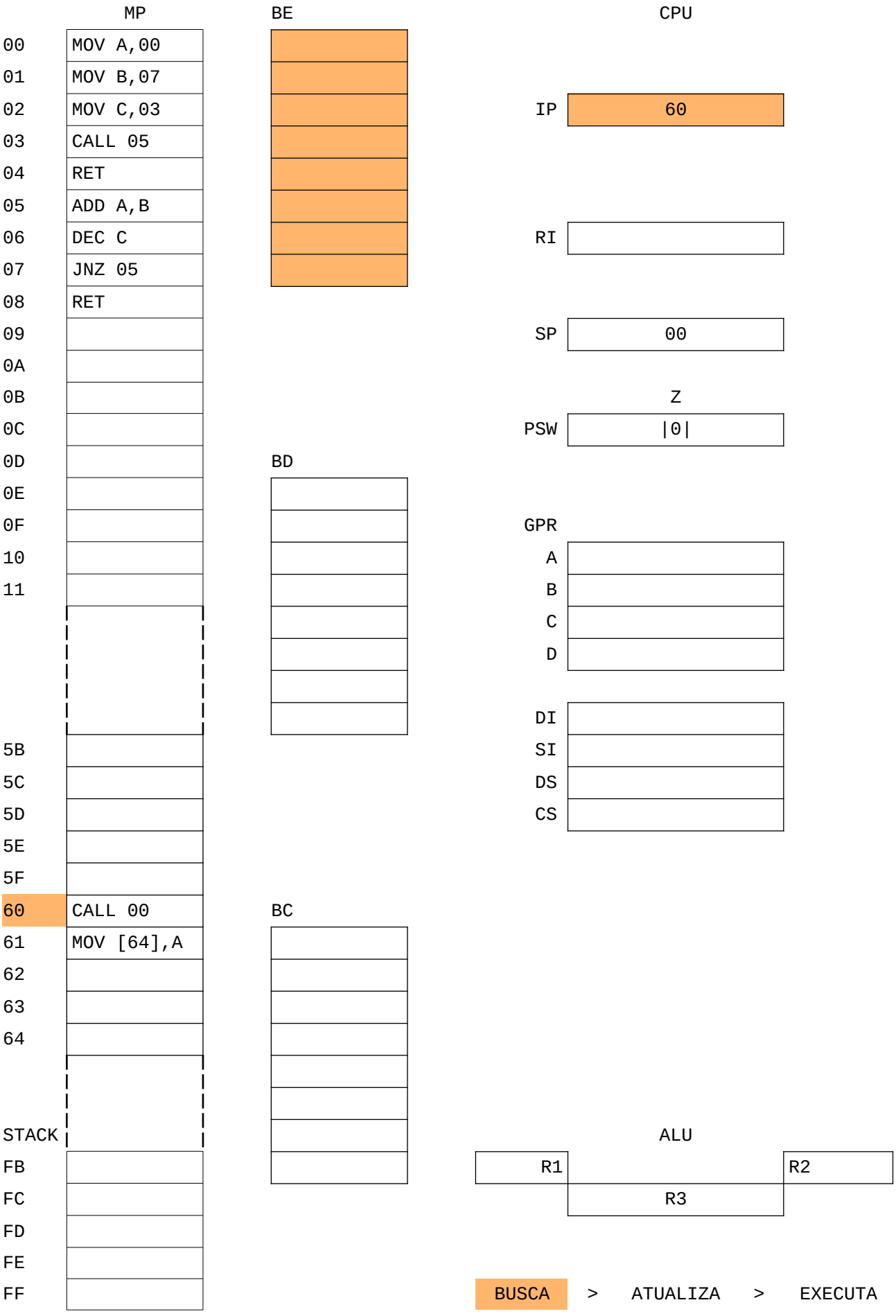
MP
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CPU
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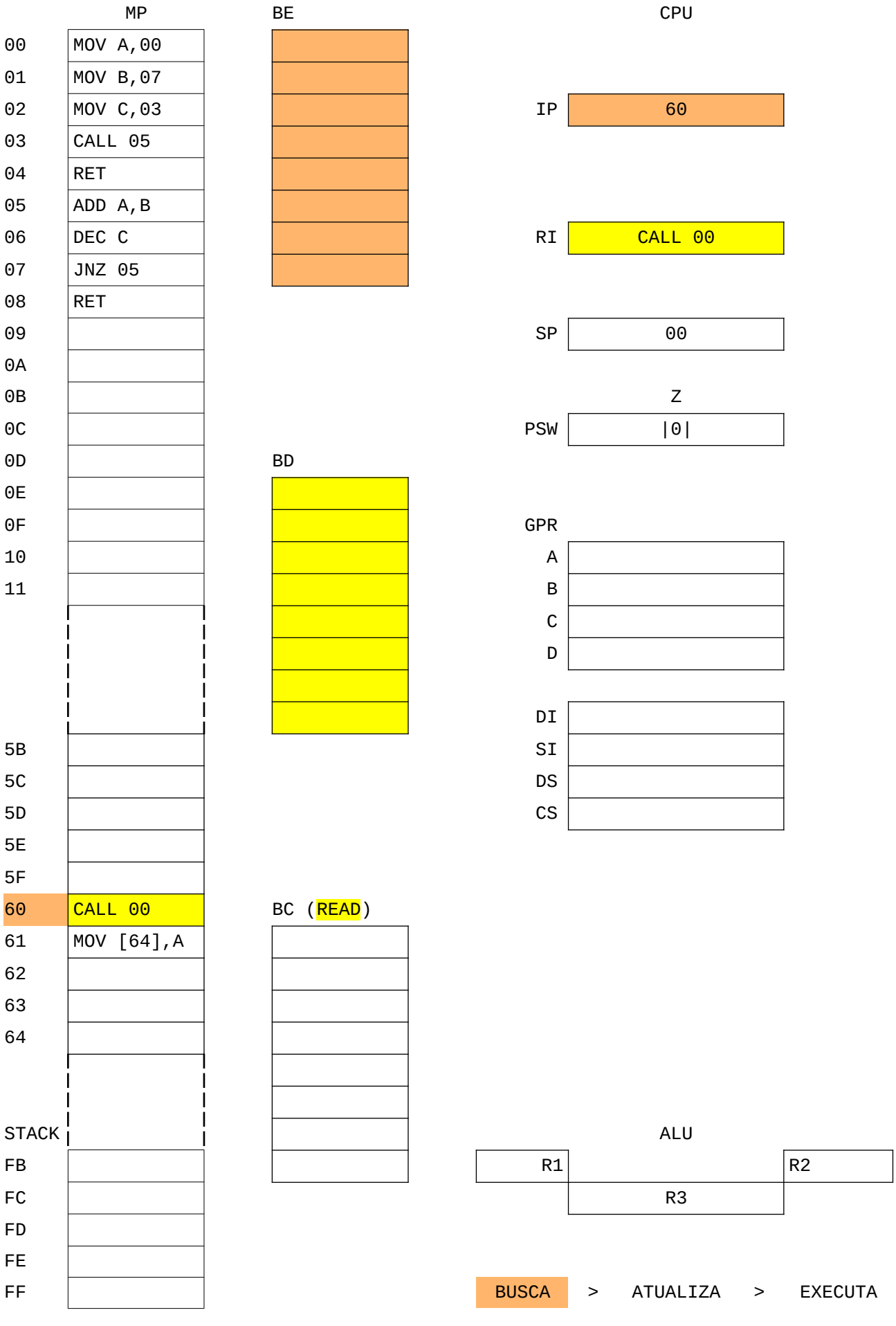
01 MOV B,07
02 MOV C,03
03 CALL 05
04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
09
0A
0B
0C
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF
IP
60
RI
SP
00
Z
PSW
|0|
BD
GPR
A
B
C
D
DI
SI
DS
CS
BC
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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02 MOV C,03
03 CALL 05
04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
09
0A
0B
0C
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF
IP
60
RI
CALL 00
SP
00
Z
PSW
|0|
BD
GPR
A
B
C
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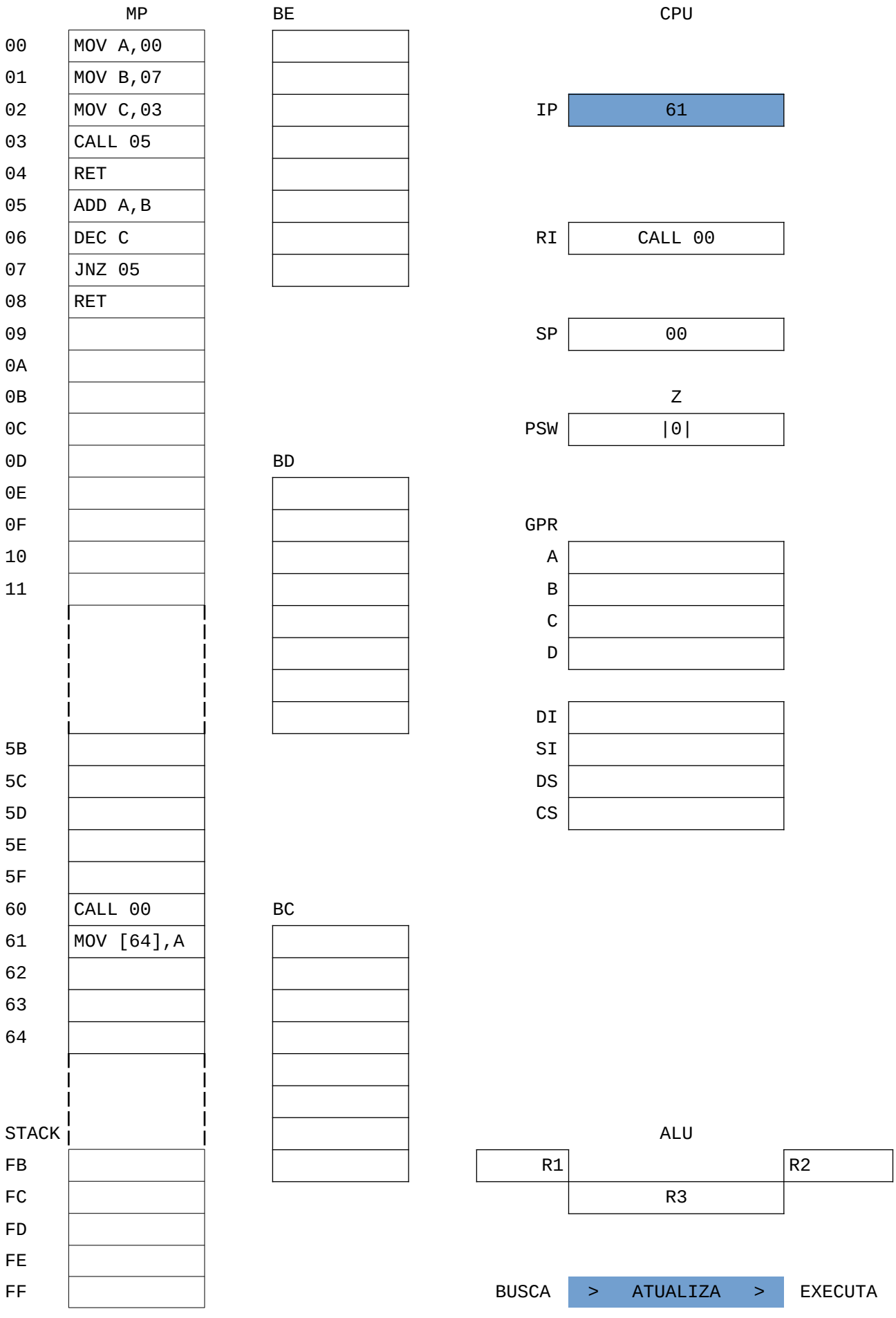
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA
>
ATUALIZA
>
EXECUTA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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
09
0A
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0C
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF
BE
BD
BC
CPU
IP 61
RI CALL 00
SP 00
Z
PSW |0|
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SI
DS
CS
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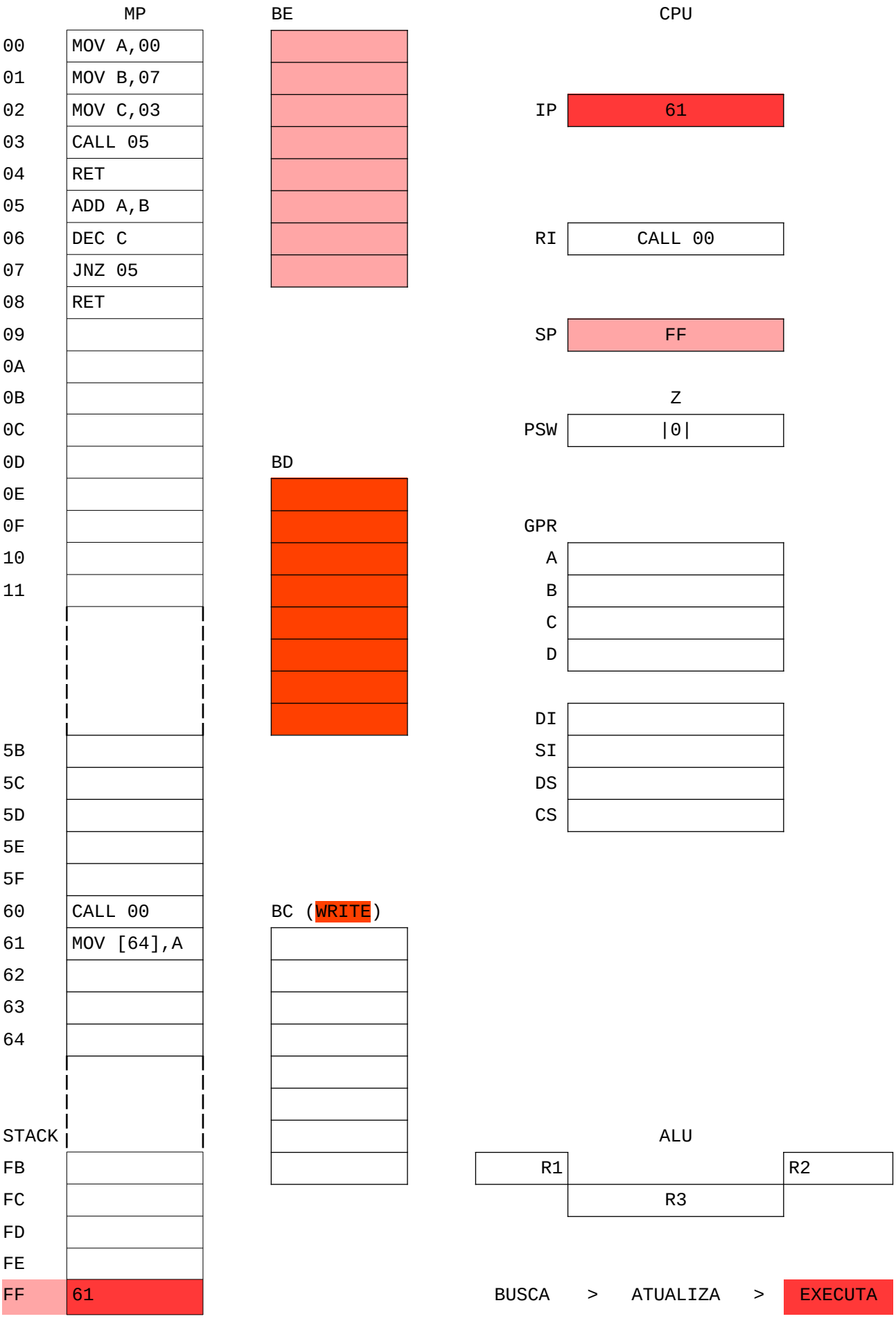
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
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0B
0C
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BE
BD
BC (WRITE)
CPU
IP 61
RI CALL 00
SP FF
Z
PSW |0|
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C
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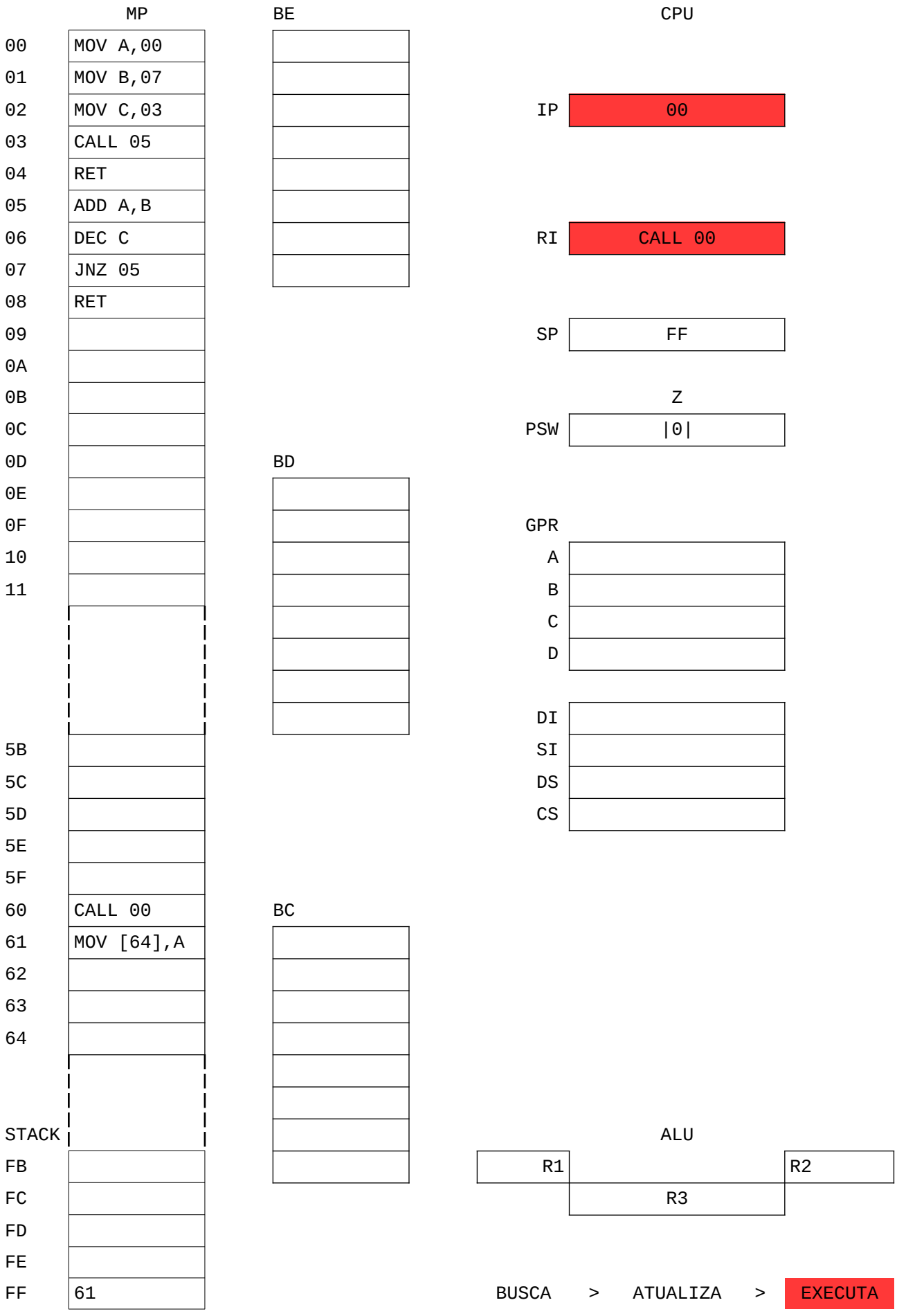
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
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06 DEC C
07 JNZ 05
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5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
IP 00
RI CALL 00
SP FF
Z
PSW |0|
BD
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R3
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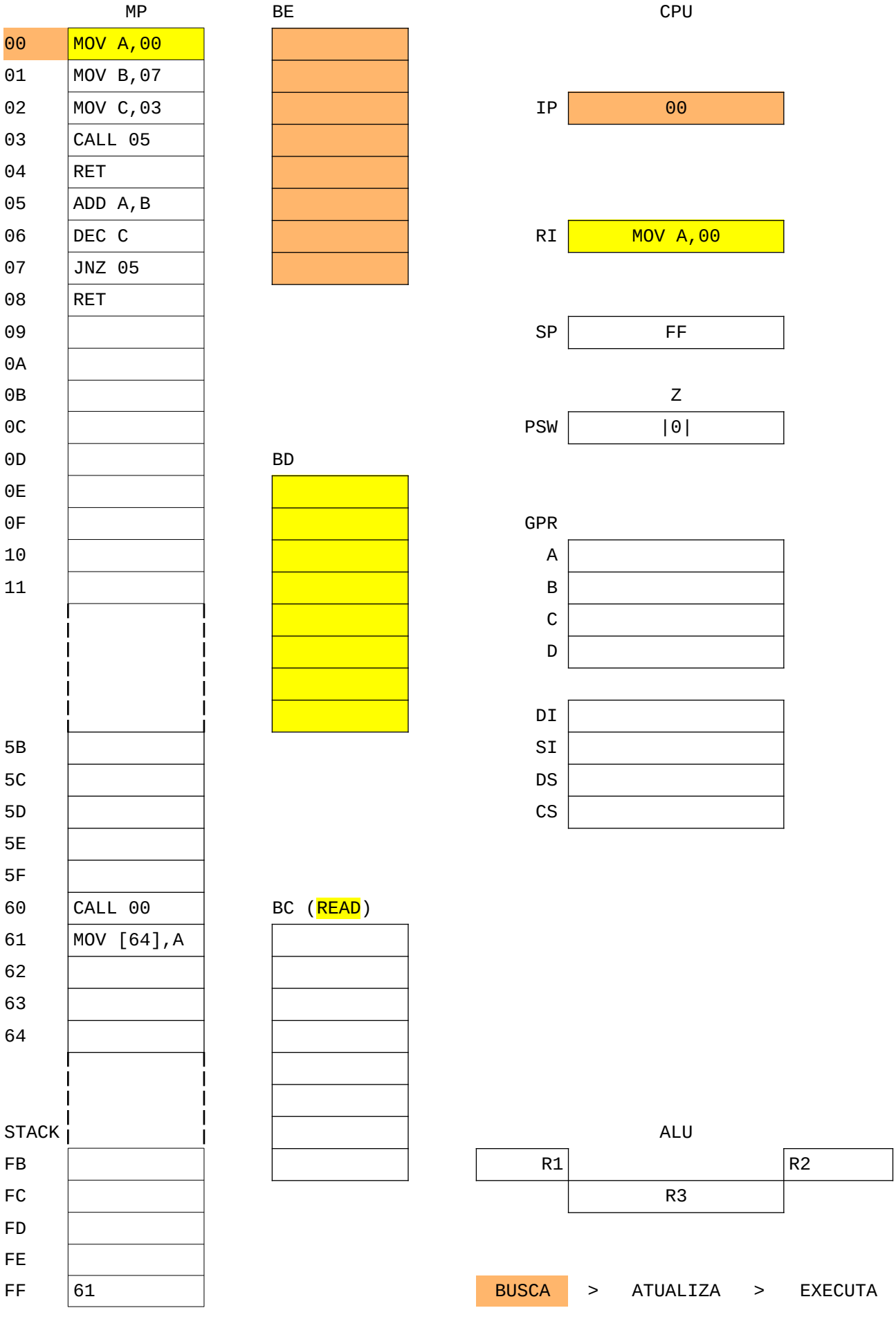
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02 MOV C,03
03 CALL 05
04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
09
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5B
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5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
IP 00
RI MOV A,00
SP FF
Z
PSW |0|
BD
GPR
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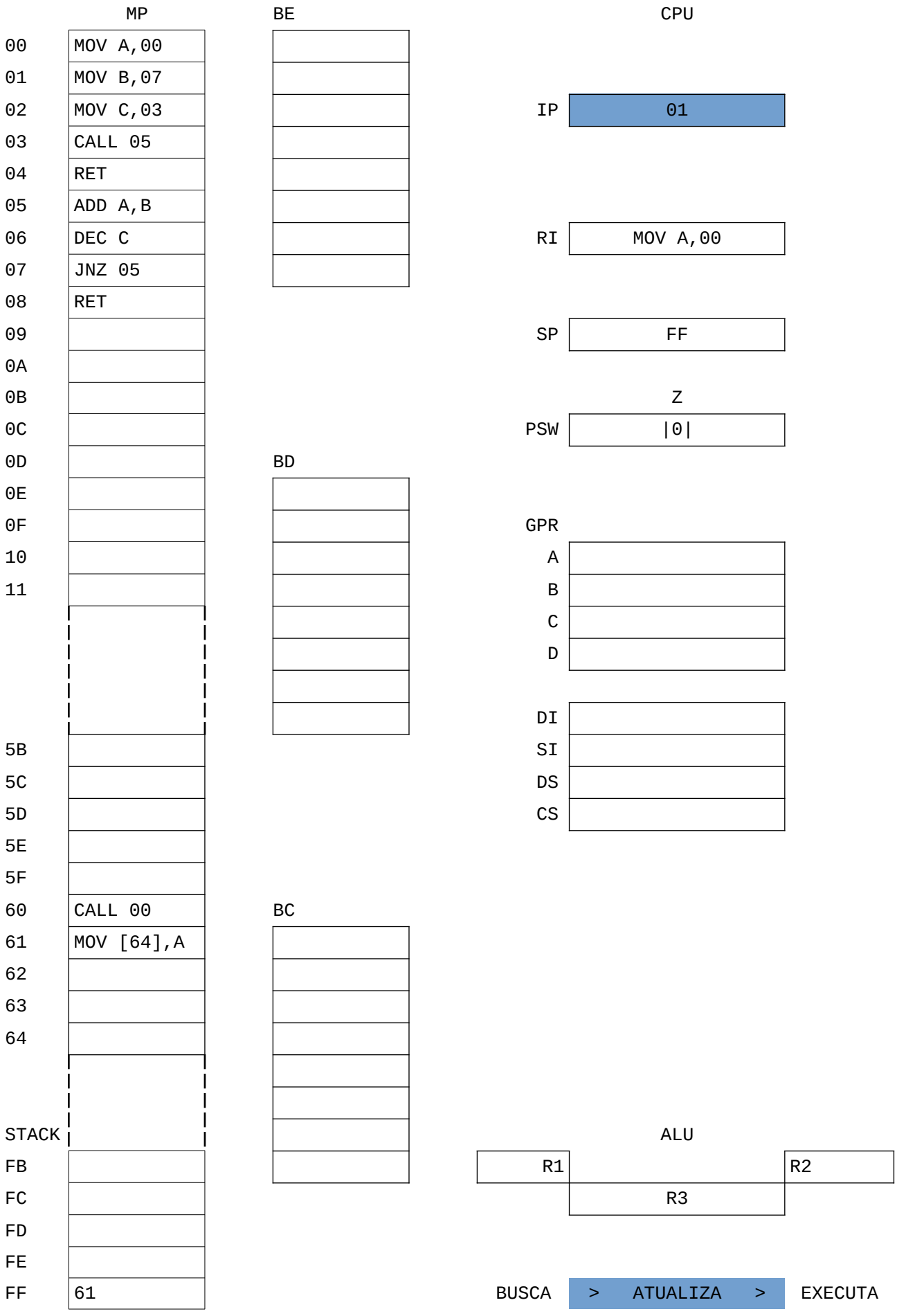
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
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06 DEC C
07 JNZ 05
08 RET
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5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
IP 01
RI MOV A,00
SP FF
Z
PSW |0|
BD
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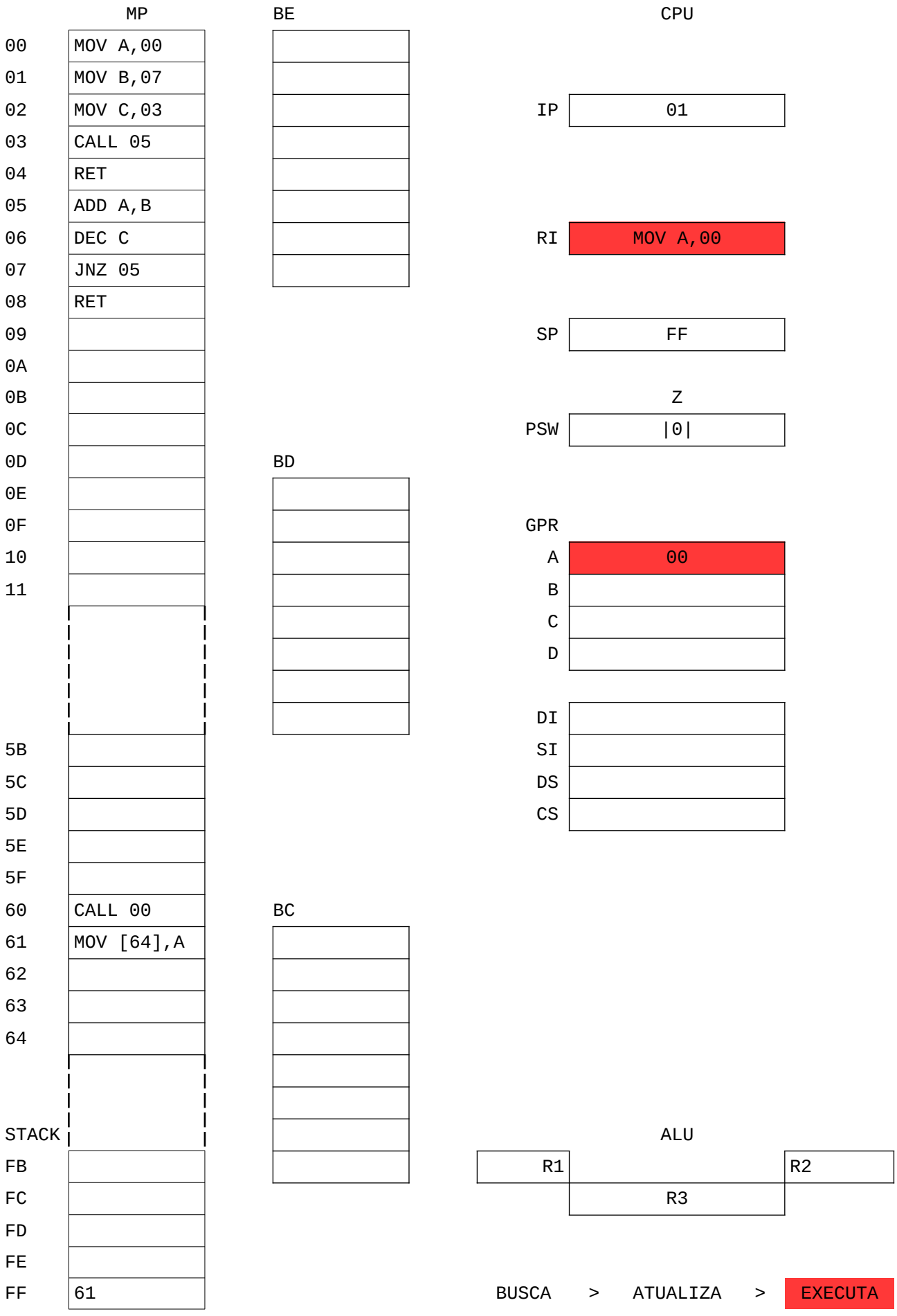
CS
BC
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
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06 DEC C
07 JNZ 05
08 RET
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5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BD
BC
IP 01
RI MOV A,00
SP FF
Z
PSW |0|
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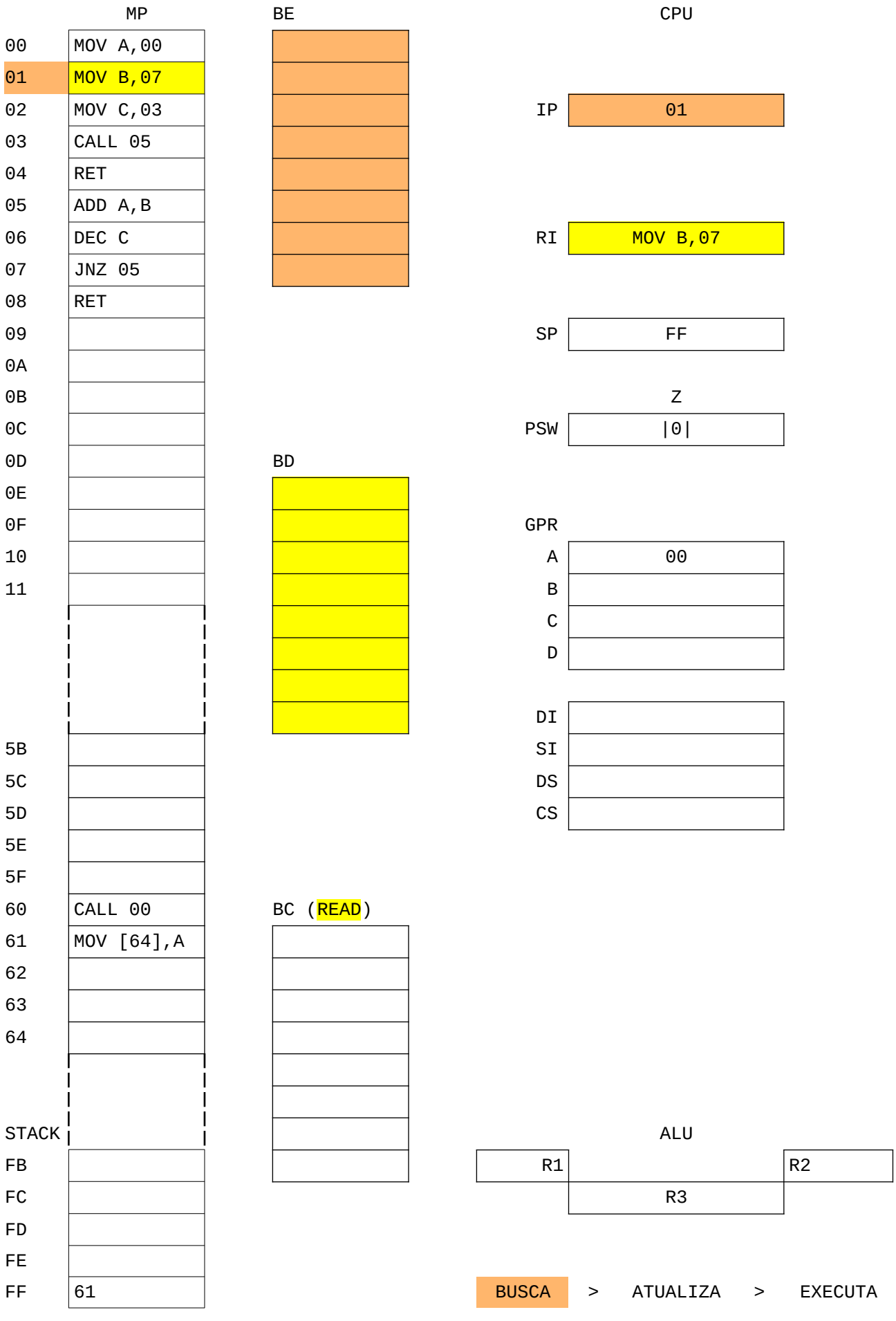
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
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06 DEC C
07 JNZ 05
08 RET
09
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0E
0F
10
11
5B
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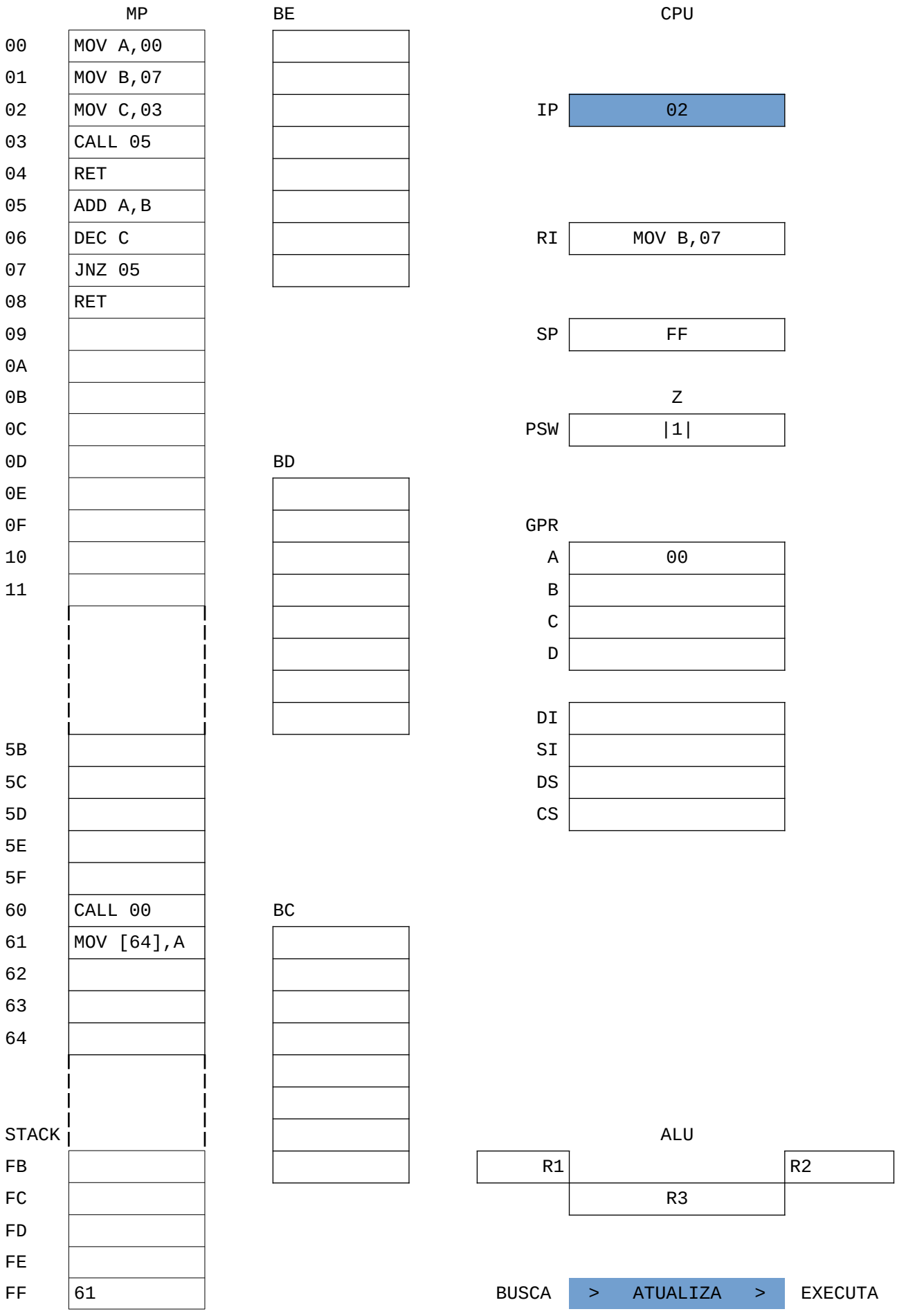
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
IP 01
RI MOV B,07
SP FF
Z
PSW |0|
BD
GPR
A 00
B
C
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
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0A
0B
0C
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BE
BD
BC
CPU
IP 02
RI MOV B,07
SP FF
Z
PSW |1|
GPR
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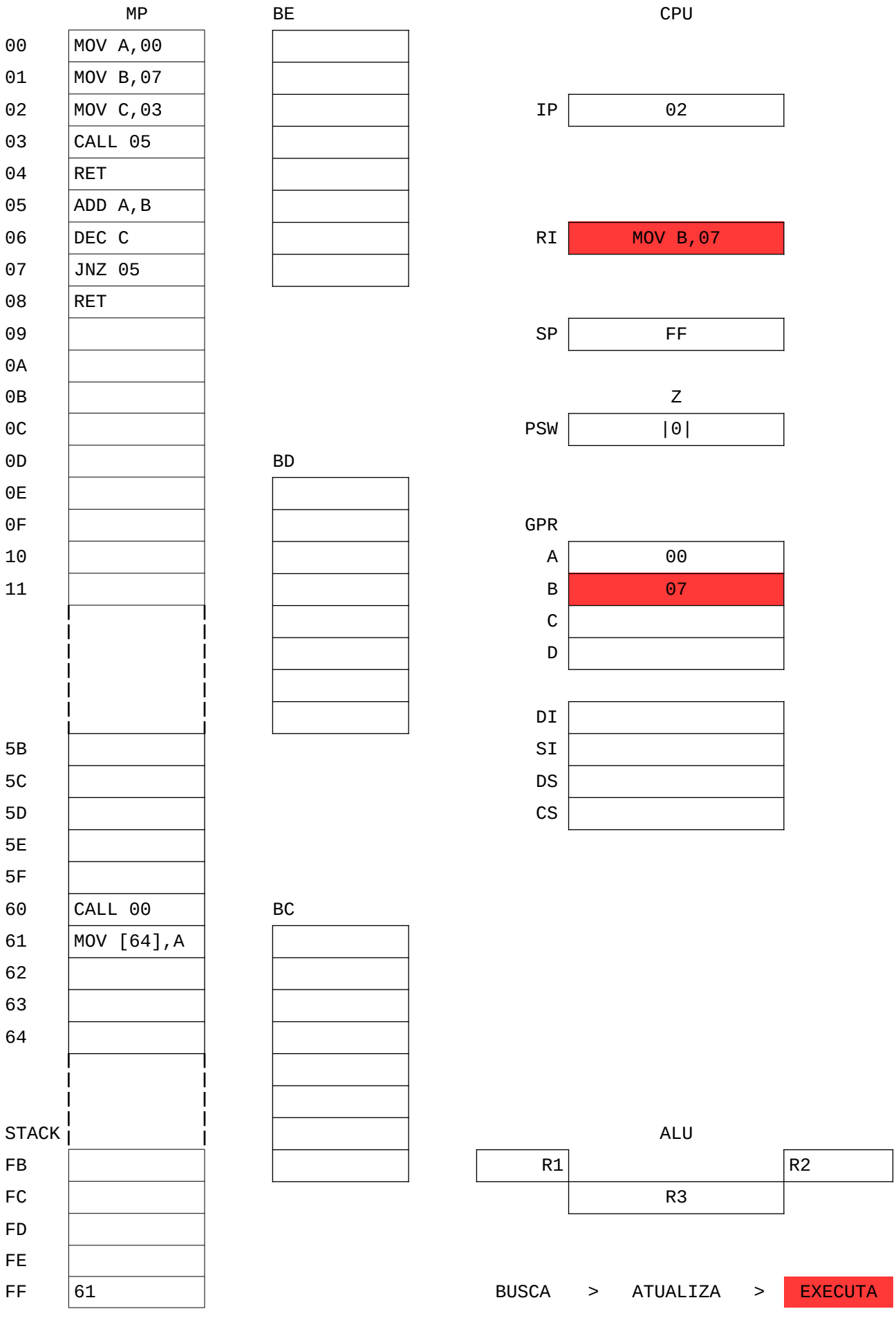
B
C
D
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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06 DEC C
07 JNZ 05
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11
5B
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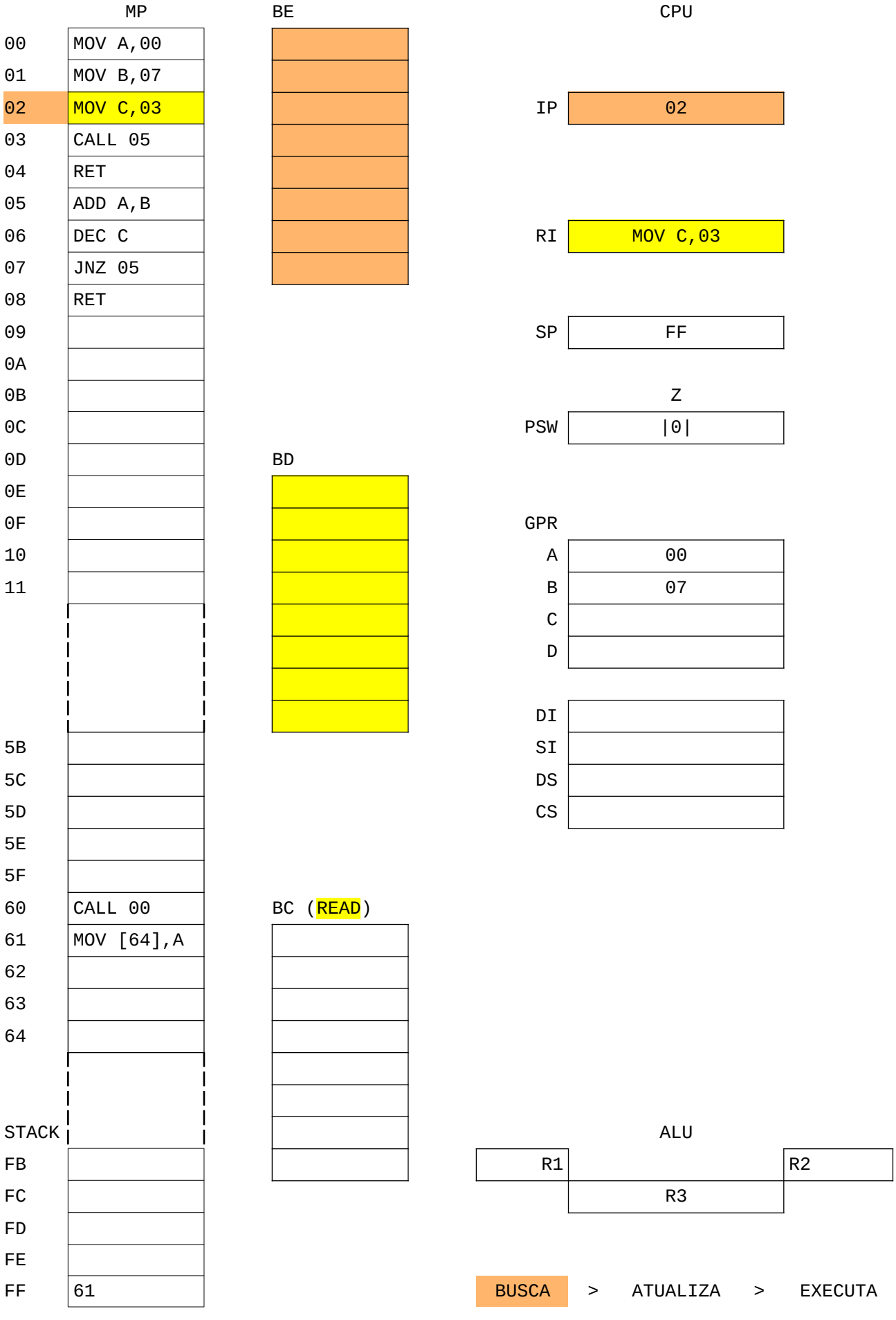
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BD
BC
IP 02
RI MOV B,07
SP FF
Z
PSW |0|
GPR
A 00
B 07
C
D
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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02 MOV C,03
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04 RET
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06 DEC C
07 JNZ 05
08 RET
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0B
0C
0D
0E
0F
10
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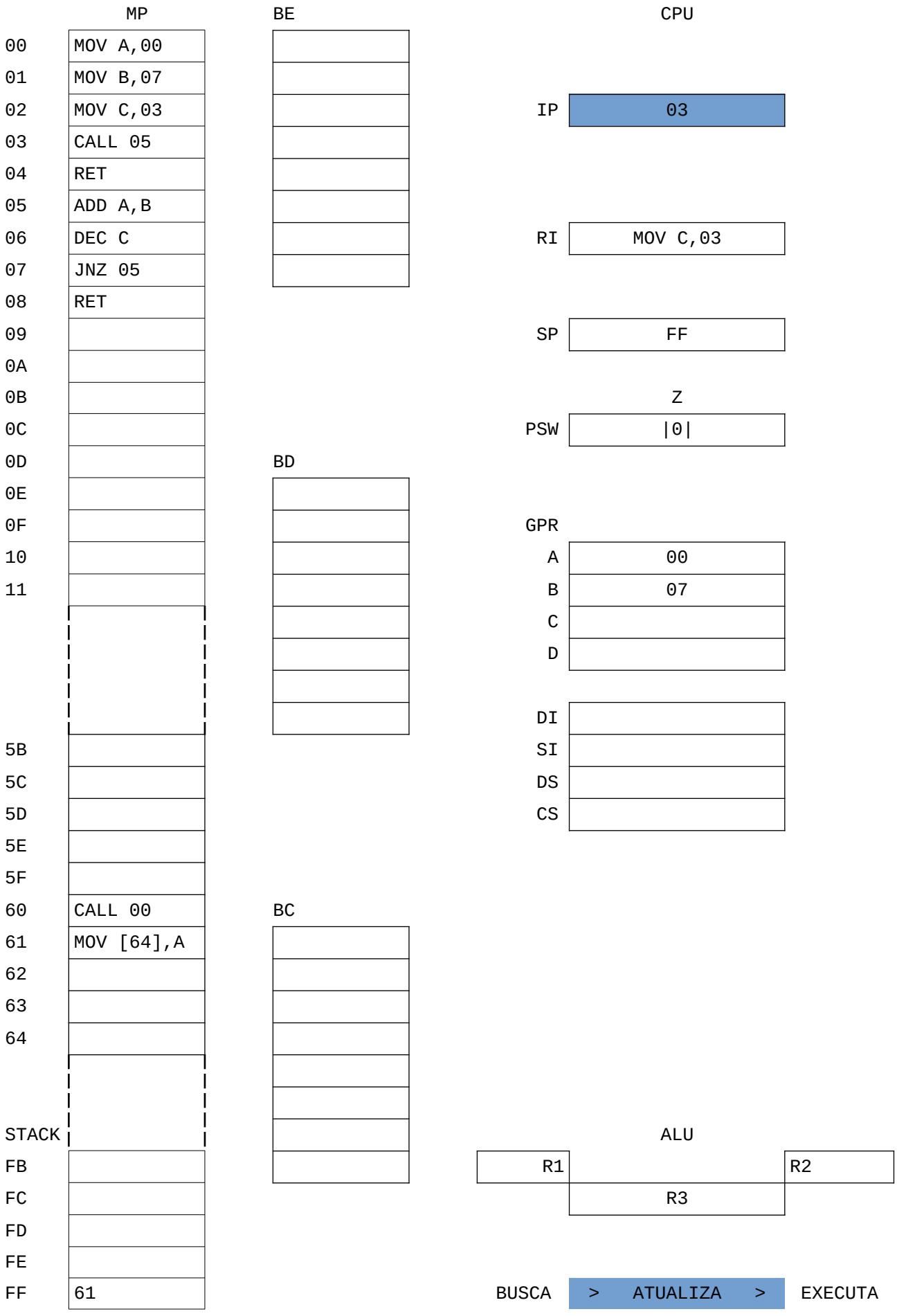
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
IP 02
RI MOV C,03
SP FF
Z
PSW |0|
BD
GPR
A 00
B 07
C
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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02 MOV C,03
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04 RET
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06 DEC C
07 JNZ 05
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0B
0C
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BD
BC
IP 03
RI MOV C,03
SP FF
Z
PSW |0|
GPR
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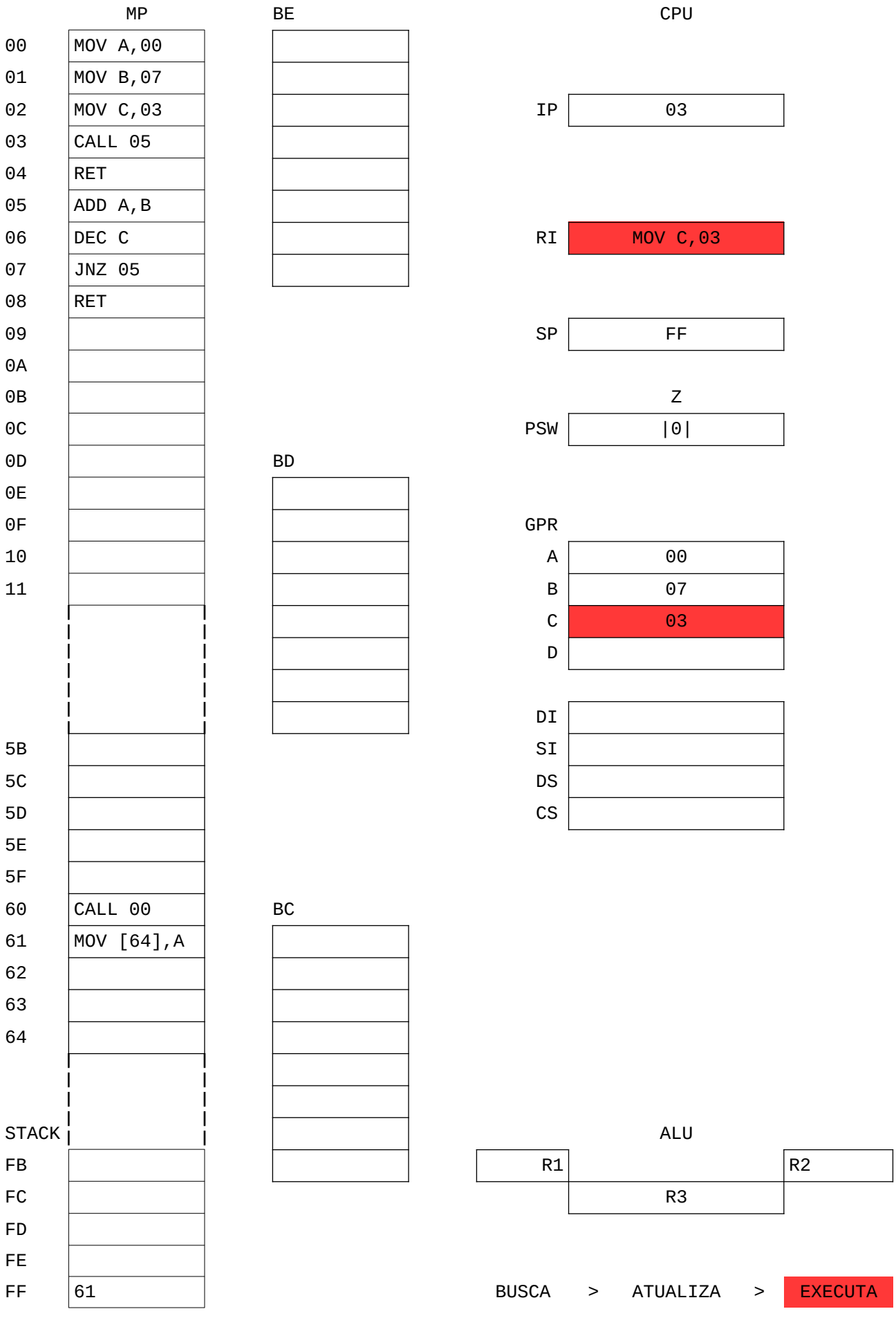
B 07
C
D
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
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0F
10
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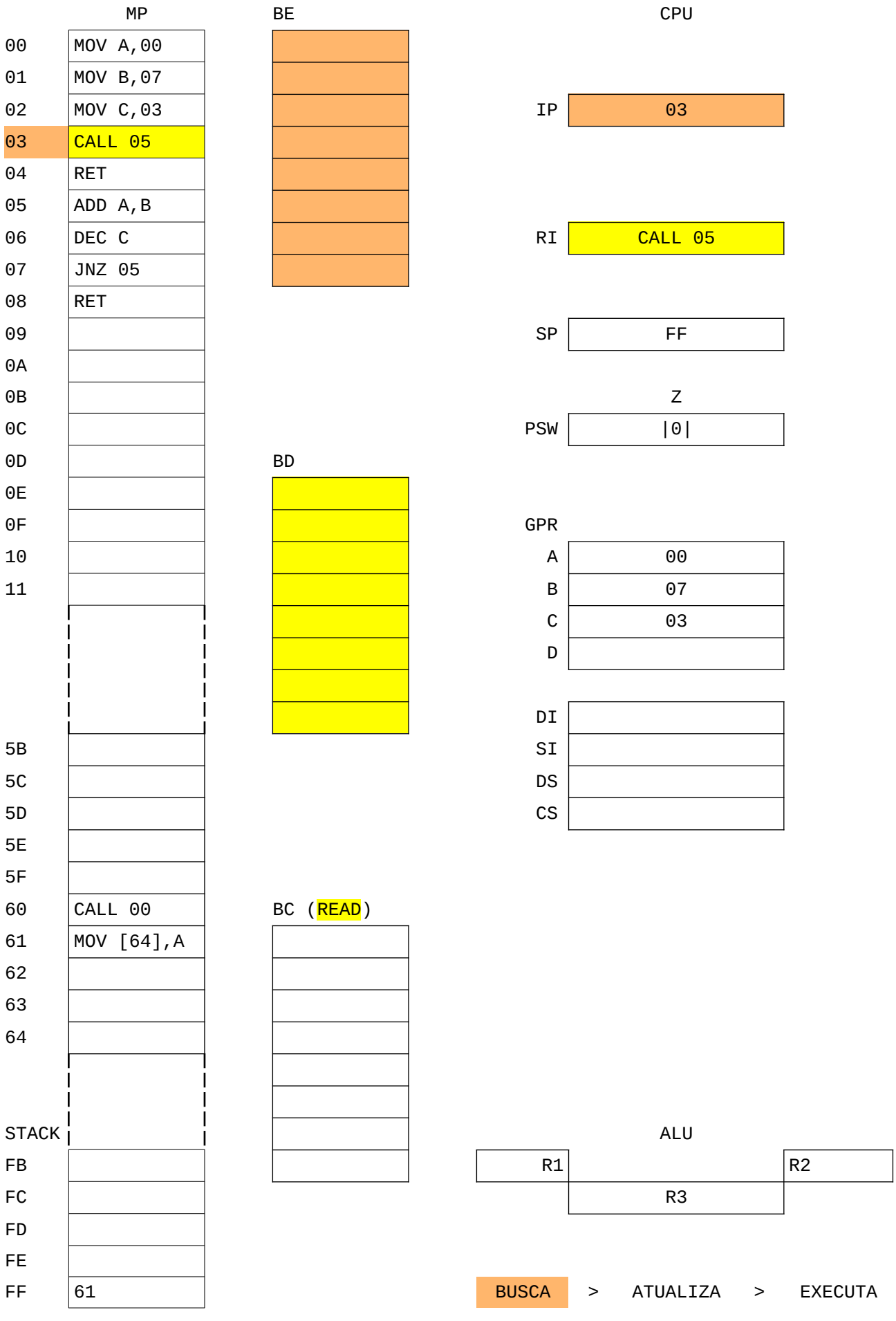
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BD
BC
IP 03
RI MOV C,03
SP FF
Z
PSW |0|
GPR
A 00
B 07
C 03
D
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
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0E
0F
10
11
5B
5C
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5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
BE
BD
BC (READ)
CPU
IP 03
RI CALL 05
SP FF
Z
PSW |0|
GPR
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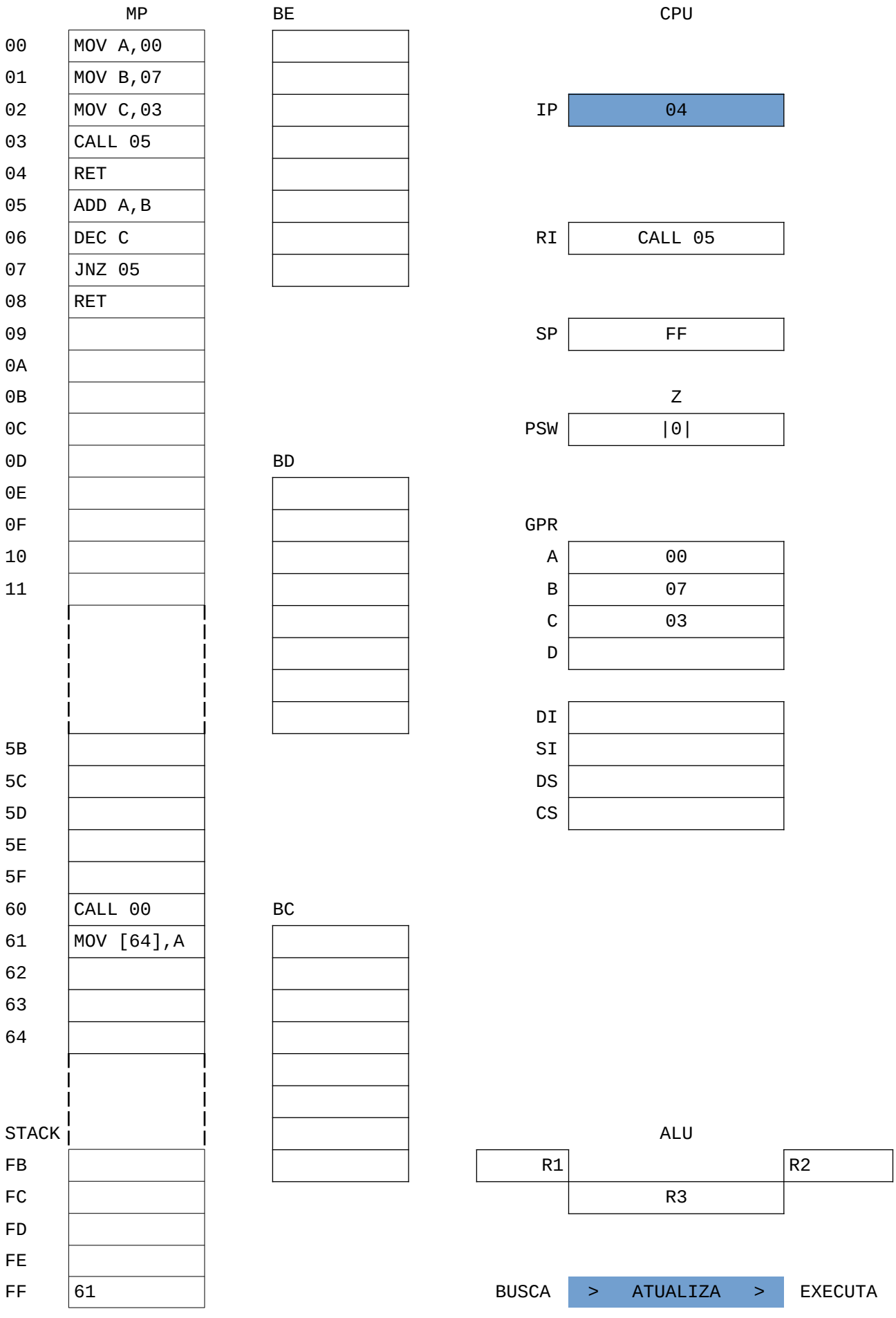
B 07
C 03
D
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
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5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE
FF 61
IP 04
RI CALL 05
SP FF
Z
PSW |0|
BD
GPR
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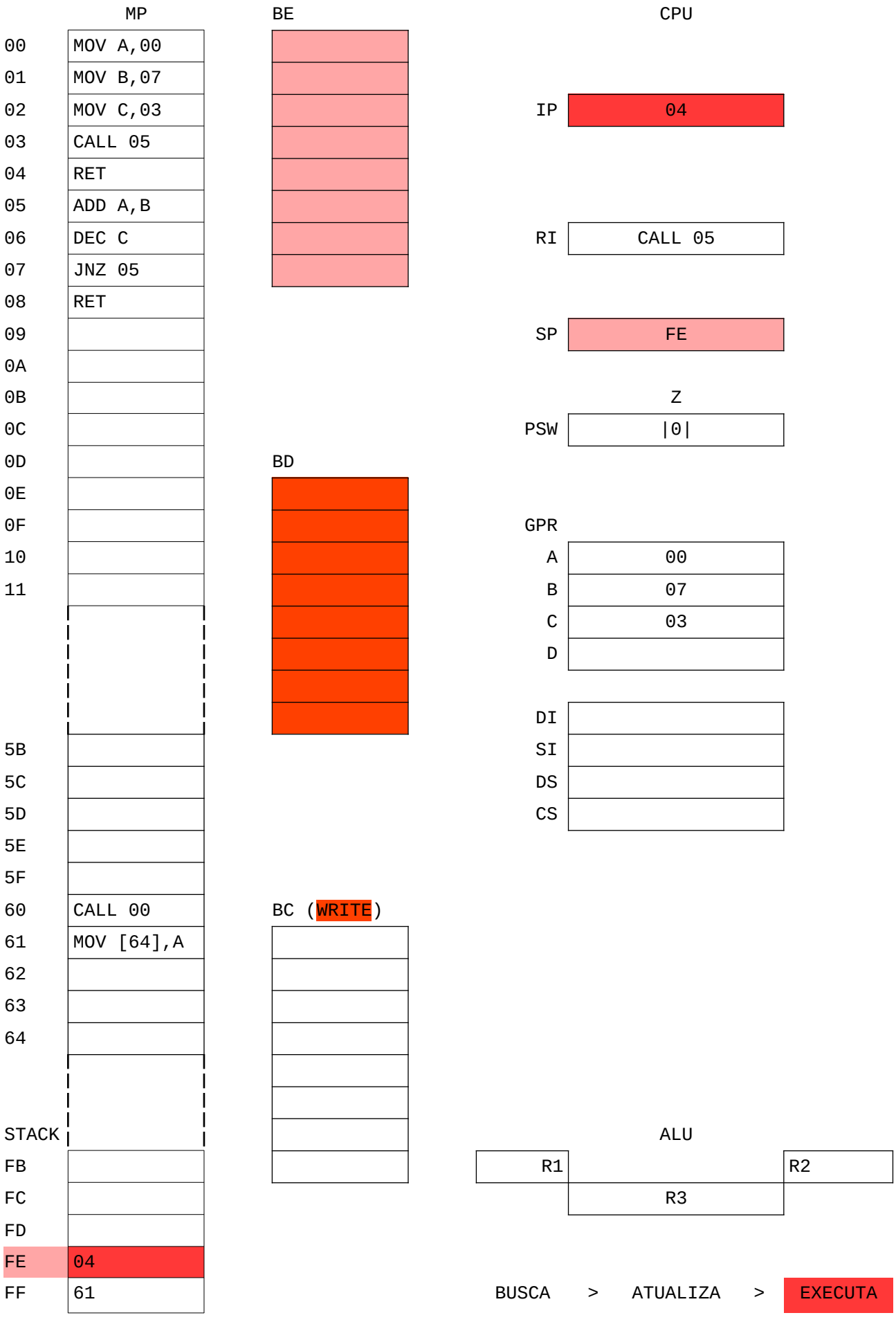
DS
CS
BC
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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CPU
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03 CALL 05
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05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
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0A
0B
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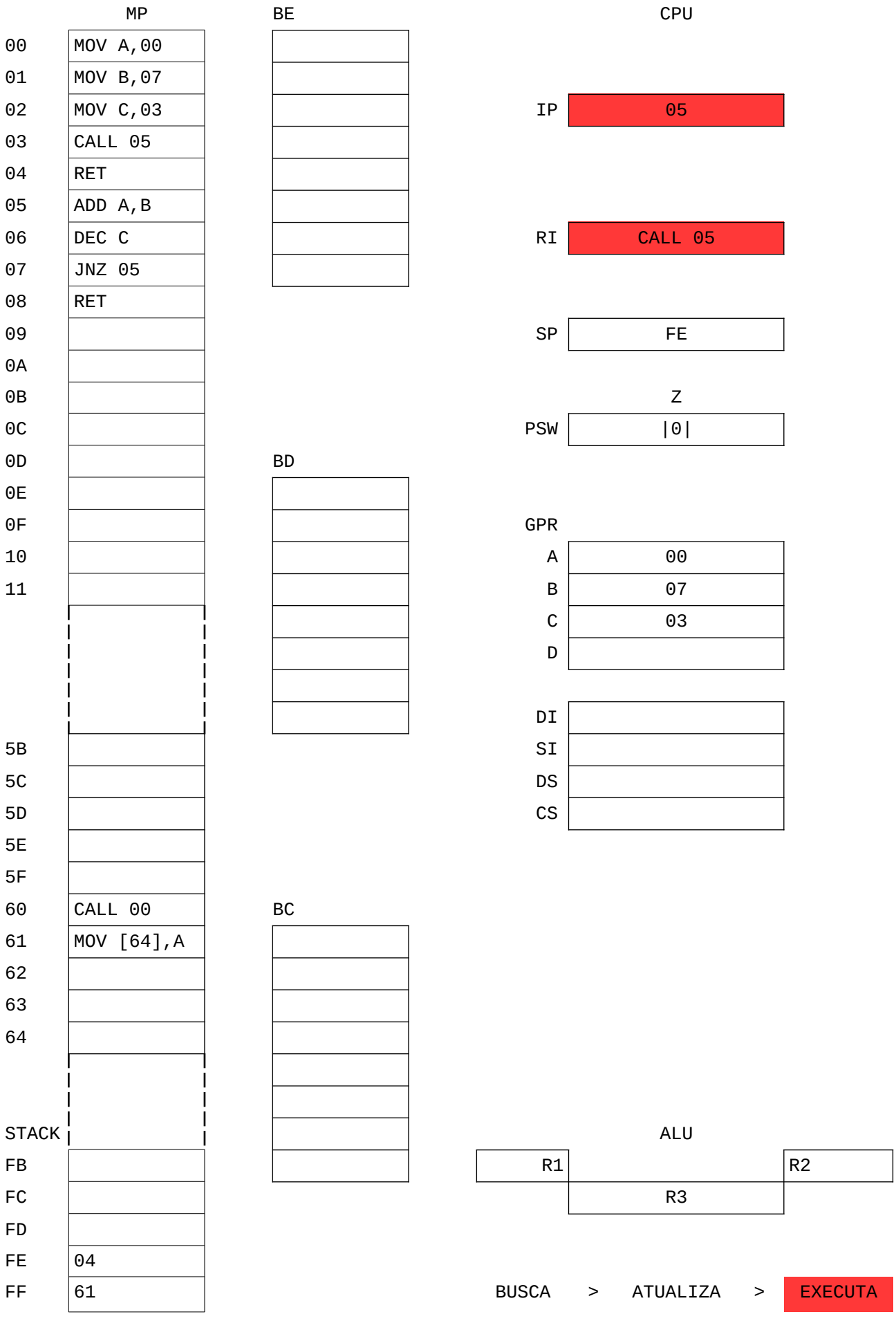
0D
0E
0F
10
11
5B
5C
5D
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 04
RI CALL 05
SP FE
Z
PSW |0|
BD
GPR
A 00
B 07
C 03
D
DI
SI
DS
CS
BC (WRITE)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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07 JNZ 05
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5F
60 CALL 00
61 MOV [64],A
62
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64
STACK
FB
FC
FD
FE 04
FF 61
BD
BC
IP 05
RI CALL 05
SP FE
Z
PSW |0|
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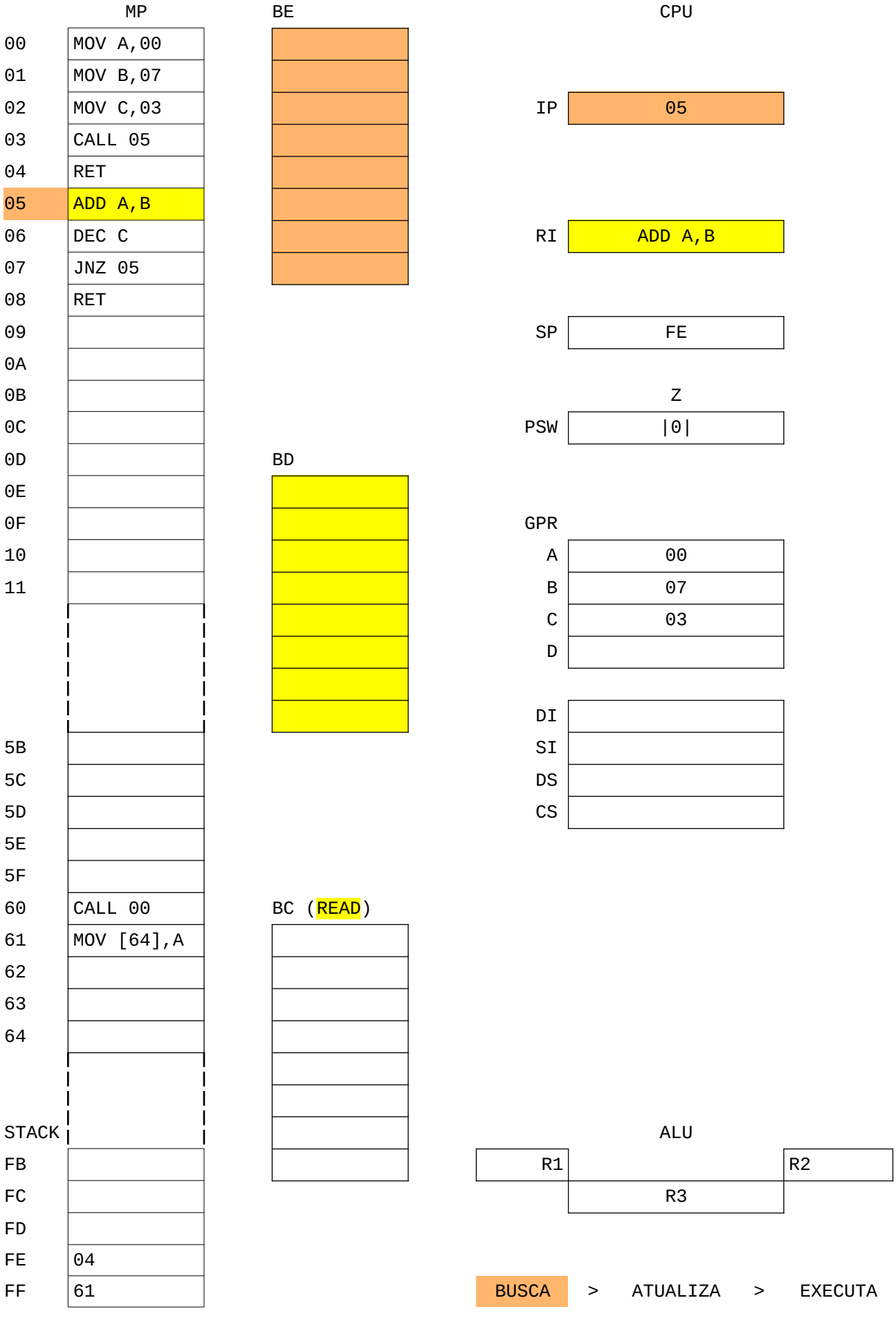
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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04 RET
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06 DEC C
07 JNZ 05
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11
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5C
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5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 05
RI ADD A,B
SP FE
Z
PSW |0|
BD
GPR
A 00
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C 03
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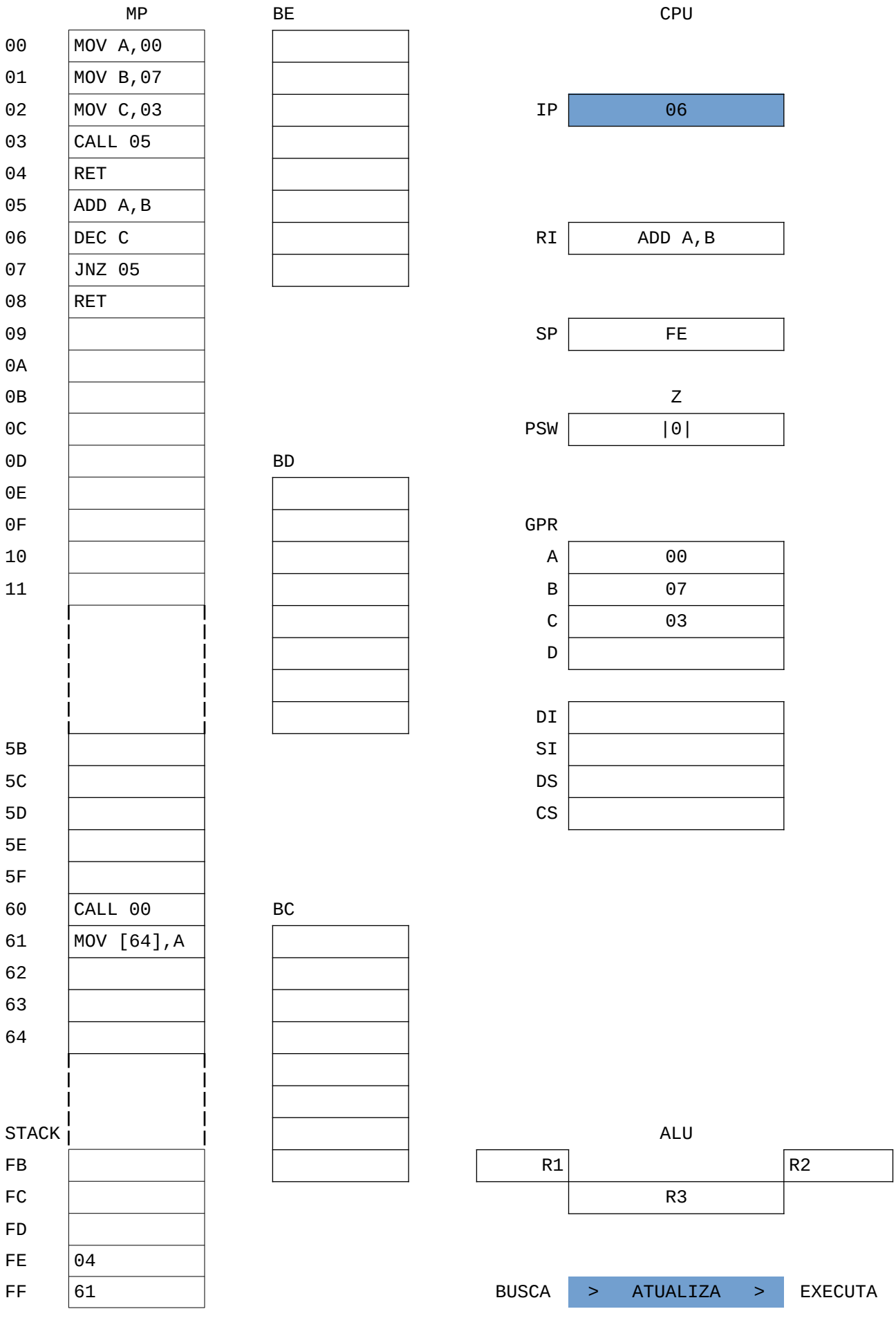
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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08 RET
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0A
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11
5B
5C
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5E
5F
60 CALL 00
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62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
BD
GPR
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CS
BC
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R1
R2
R3
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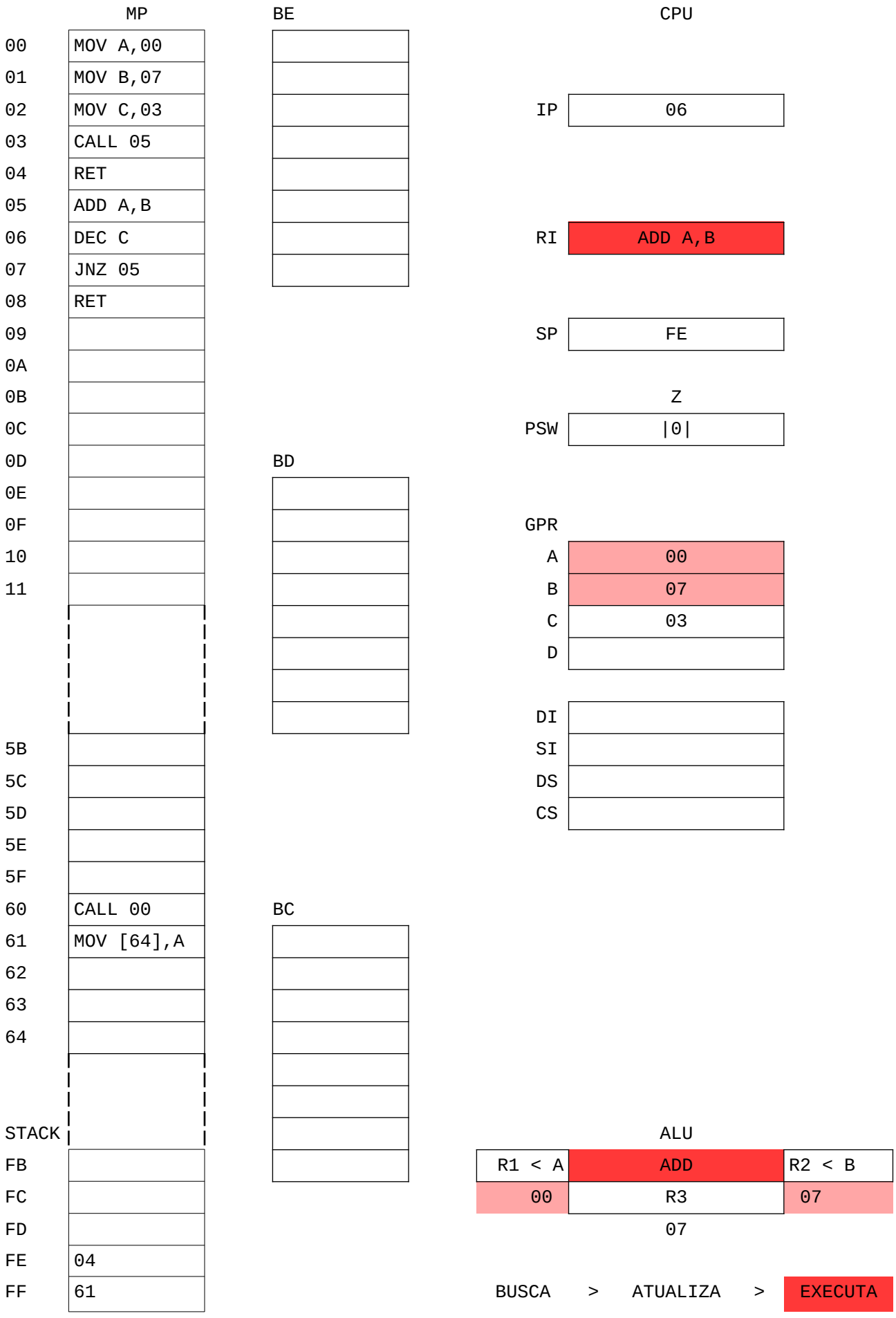
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04 RET
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06 DEC C
07 JNZ 05
08 RET
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11
5B
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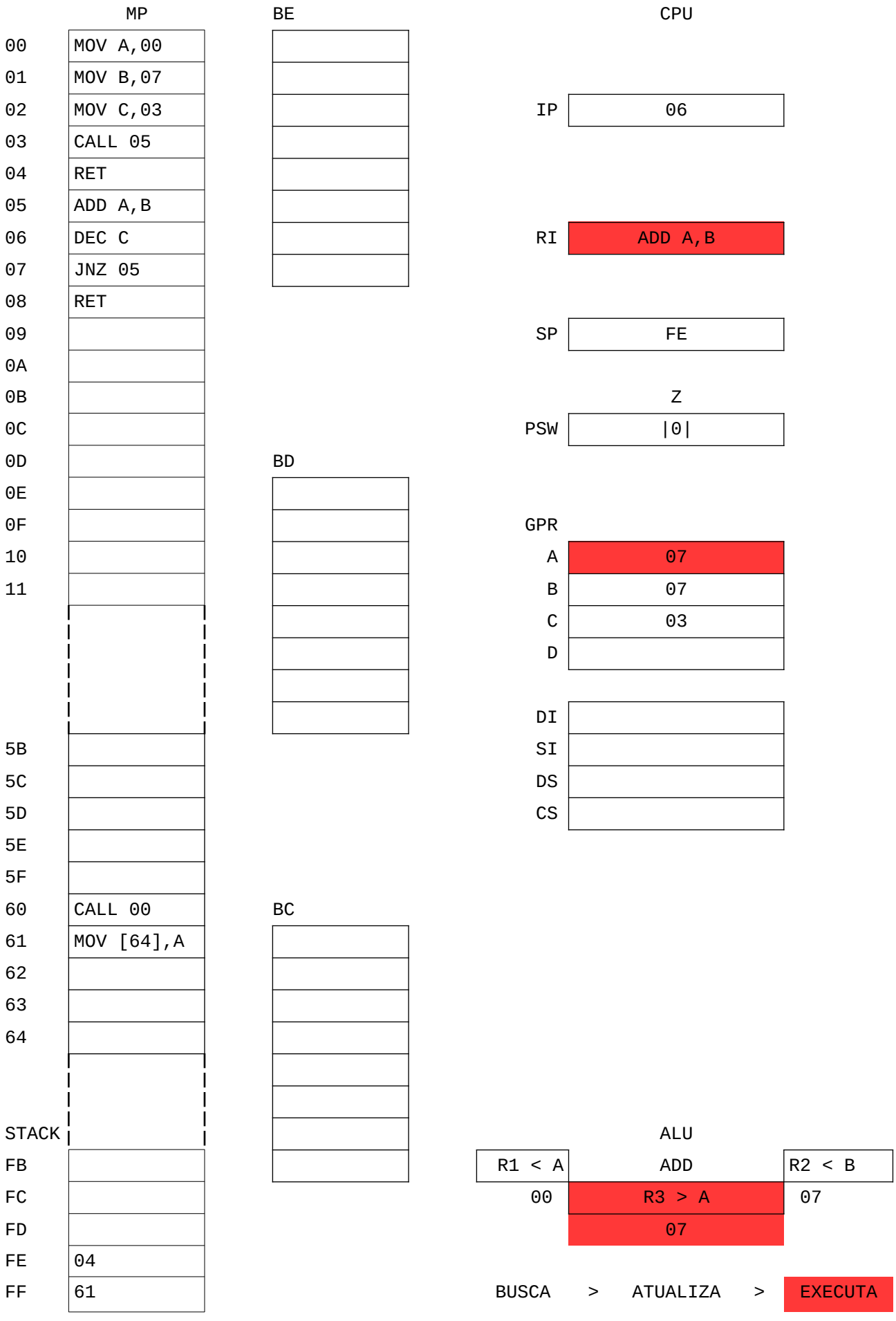
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
BD
BC
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
GPR
A 00
B 07
C 03
D
DI
SI
DS
CS
ALU
R1 < A
ADD
R2 < B
00
R3
07
07
BUSCA > ATUALIZA > EXECUTA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01 MOV B,07
02 MOV C,03
03 CALL 05
04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
09
0A
0B
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0E
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10
11
5B
5C
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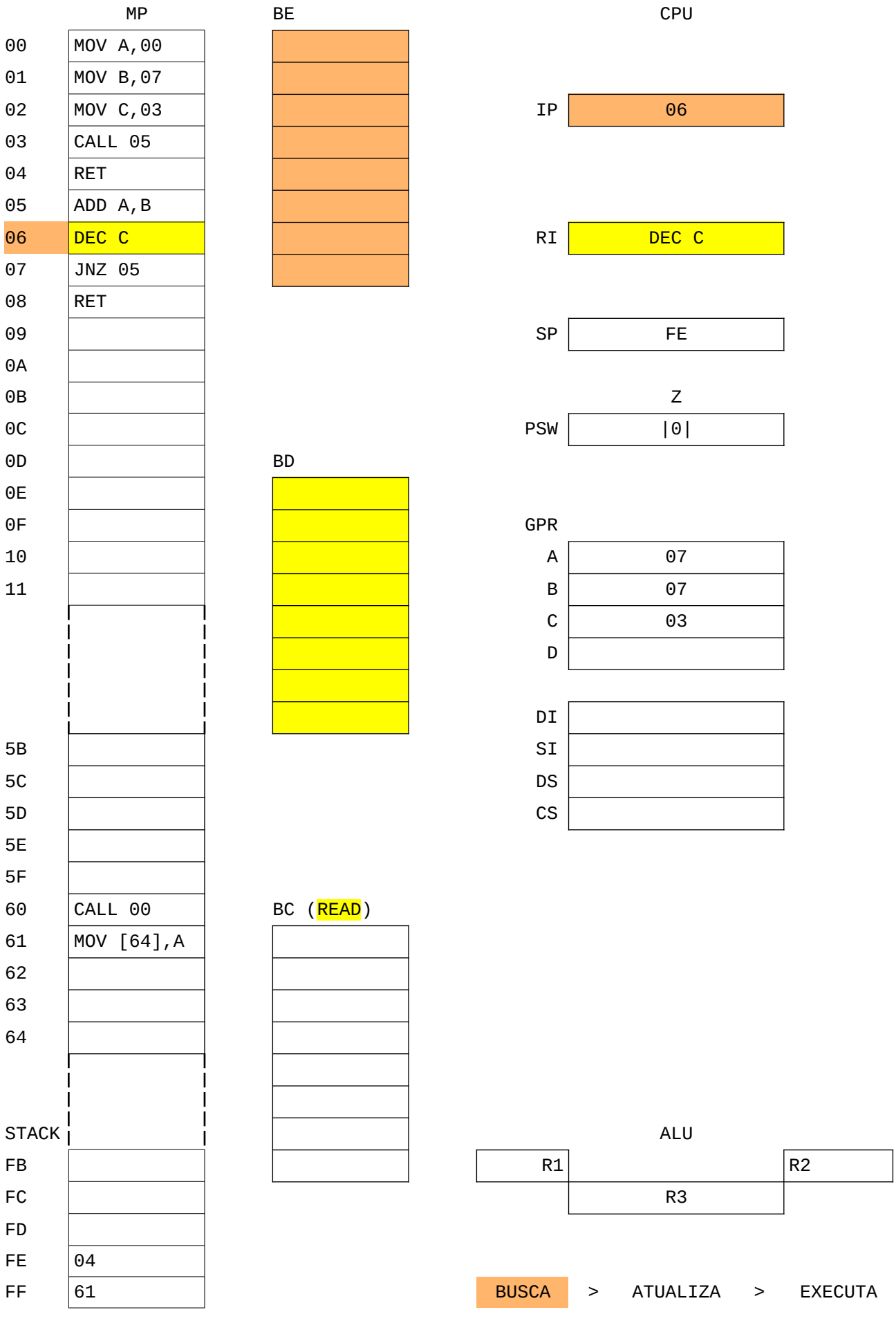
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
BD
GPR
A 07
B 07
C 03
D
DI
SI
DS
CS
BC
ALU
R1 < A
ADD
R2 < B
00
R3 > A
07
07
BUSCA > ATUALIZA > EXECUTA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06 DEC C
07 JNZ 05
08 RET
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10
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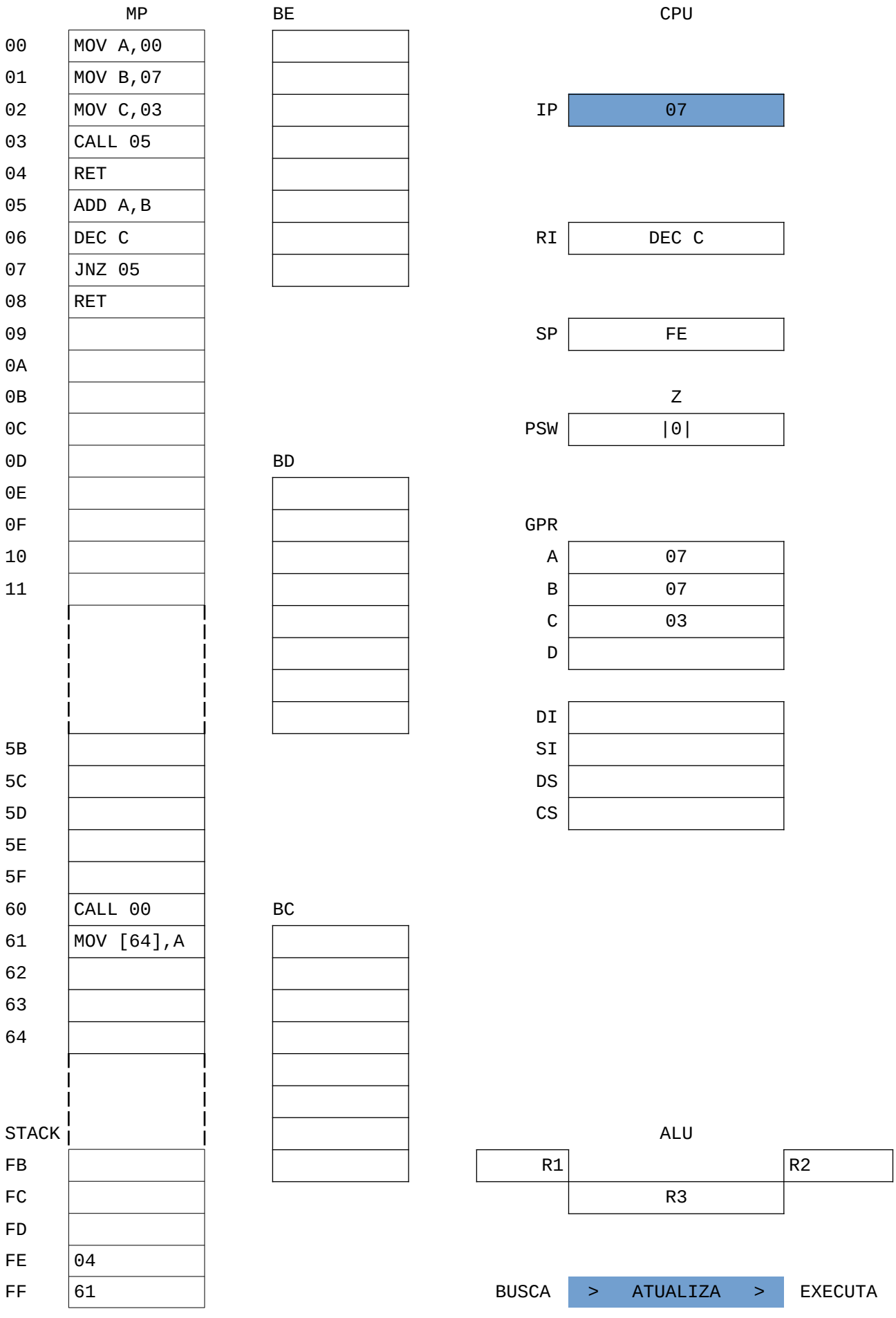
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 06
RI DEC C
SP FE
Z
PSW |0|
BD
GPR
A 07
B 07
C 03
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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06 DEC C
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08 RET
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5F
60 CALL 00
61 MOV [64],A
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STACK
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FC
FD
FE 04
FF 61
BD
BC
IP 07
RI DEC C
SP FE
Z
PSW |0|
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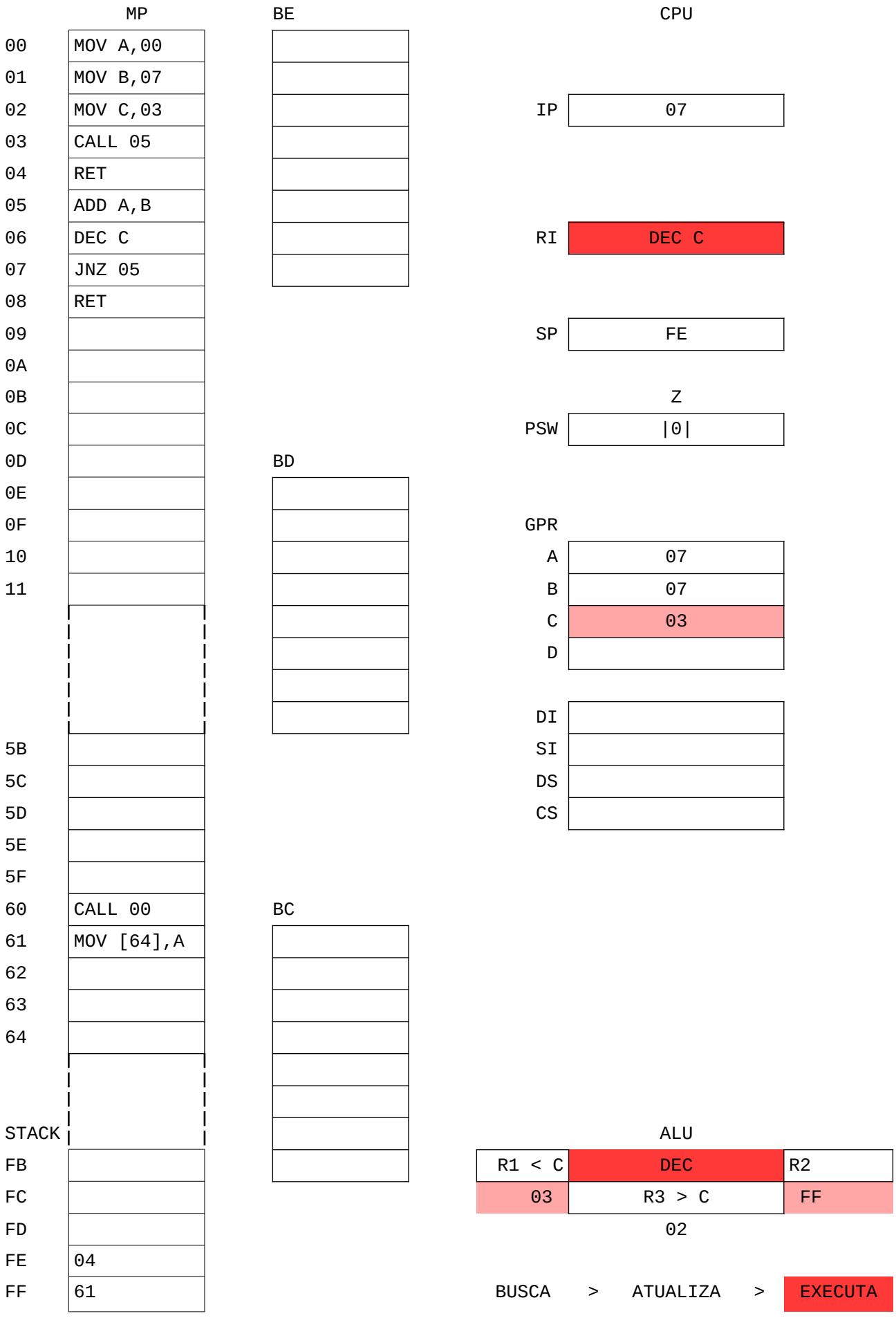
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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08 RET
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5F
60 CALL 00
61 MOV [64],A
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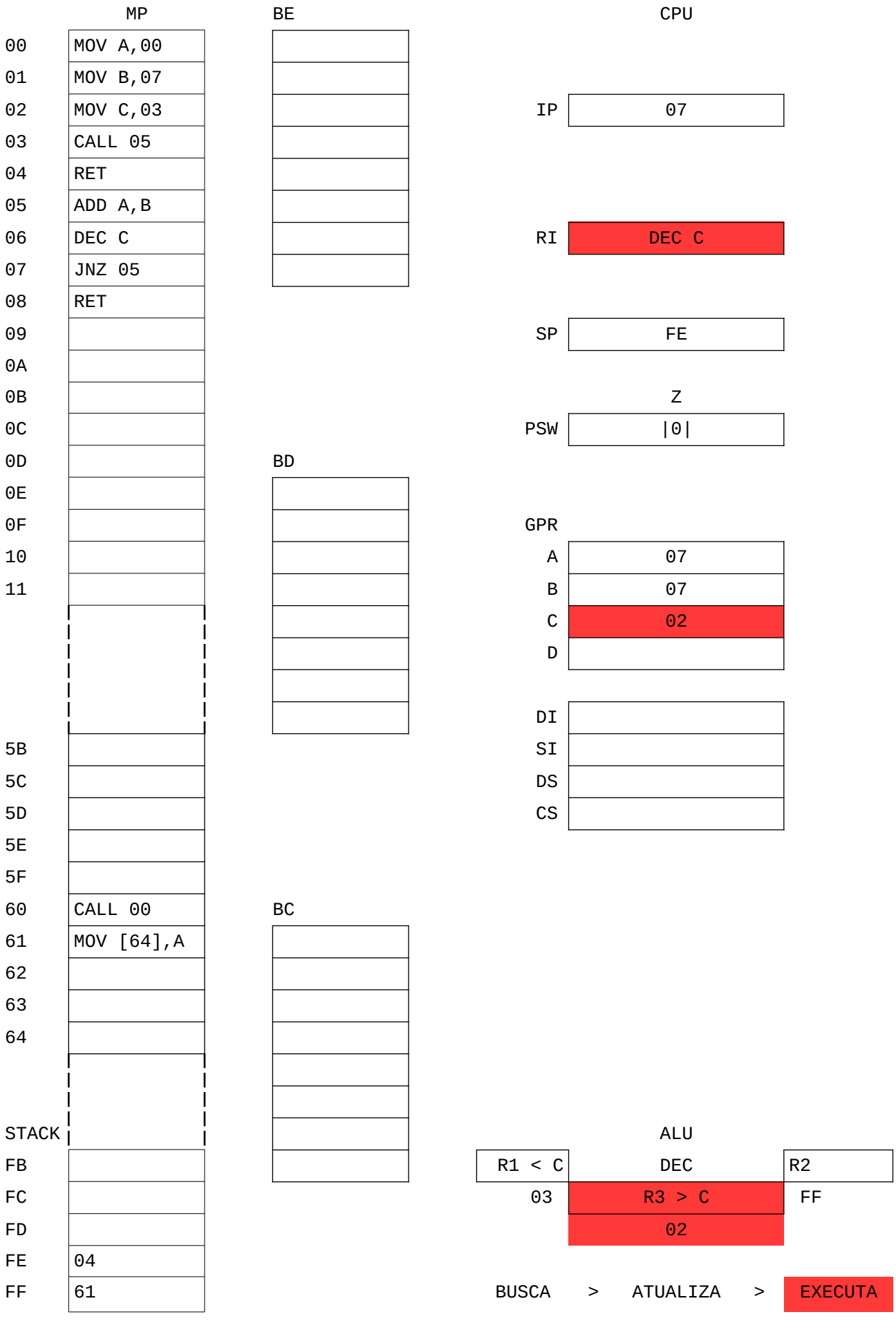
STACK
FB
FC
FD
FE 04
FF 61
BD
BC
IP 07
RI DEC C
SP FE
Z
PSW |0|
GPR
A 07
B 07
C 03
D
DI
SI
DS
CS
ALU
R1 < C
DEC
R2
03
R3 > C
FF
02
BUSCA > ATUALIZA > EXECUTA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06 DEC C
07 JNZ 05
08 RET
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0F
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5B
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60 CALL 00
61 MOV [64],A
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STACK
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FE 04
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PSW |0|
GPR
A 07
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C 02
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R1 < C
DEC
R2
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R3 > C
FF
02
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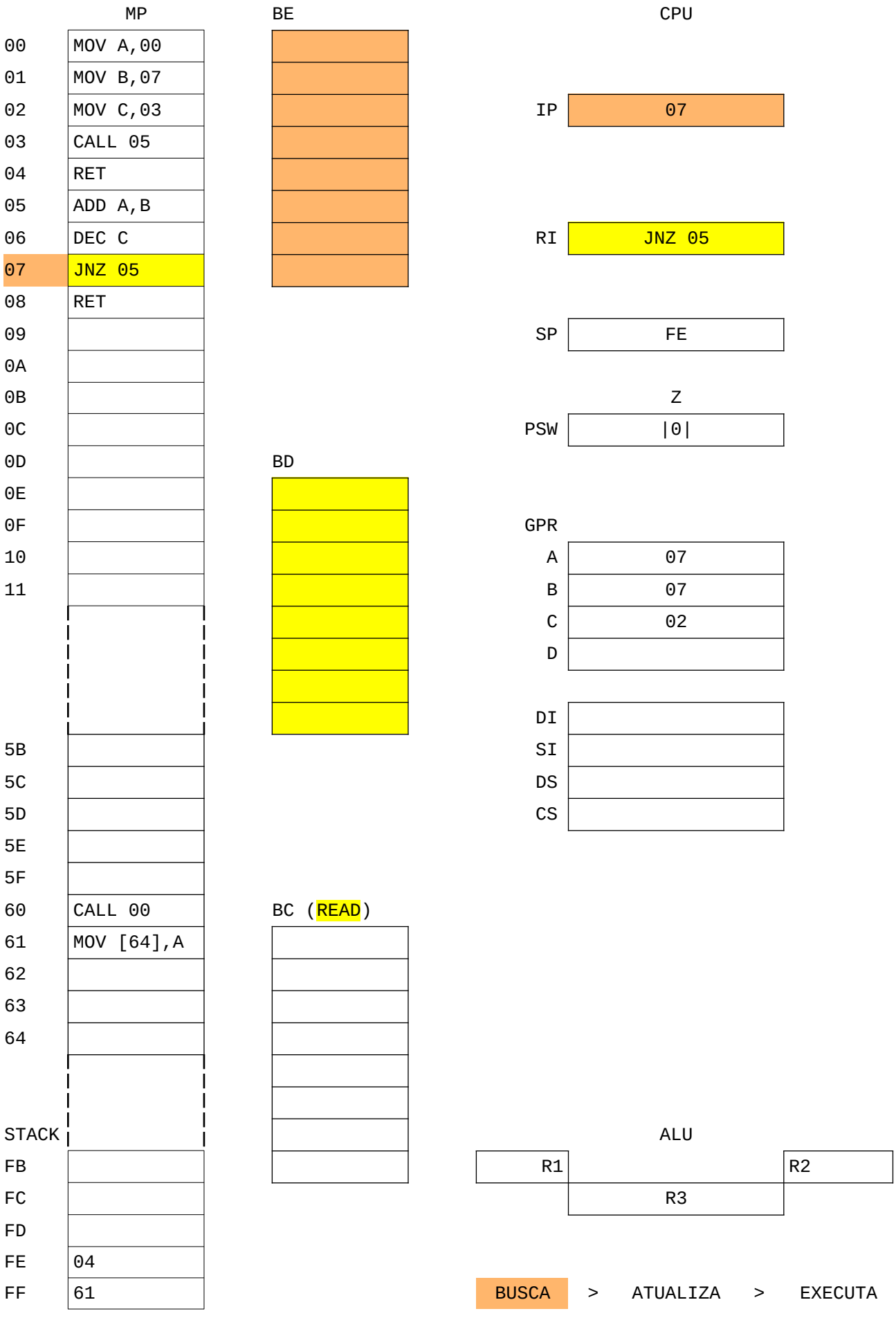
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04 RET
05 ADD A,B
06 DEC C
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08 RET
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61 MOV [64],A
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63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 07
RI JNZ 05
SP FE
Z
PSW |0|
BD
GPR
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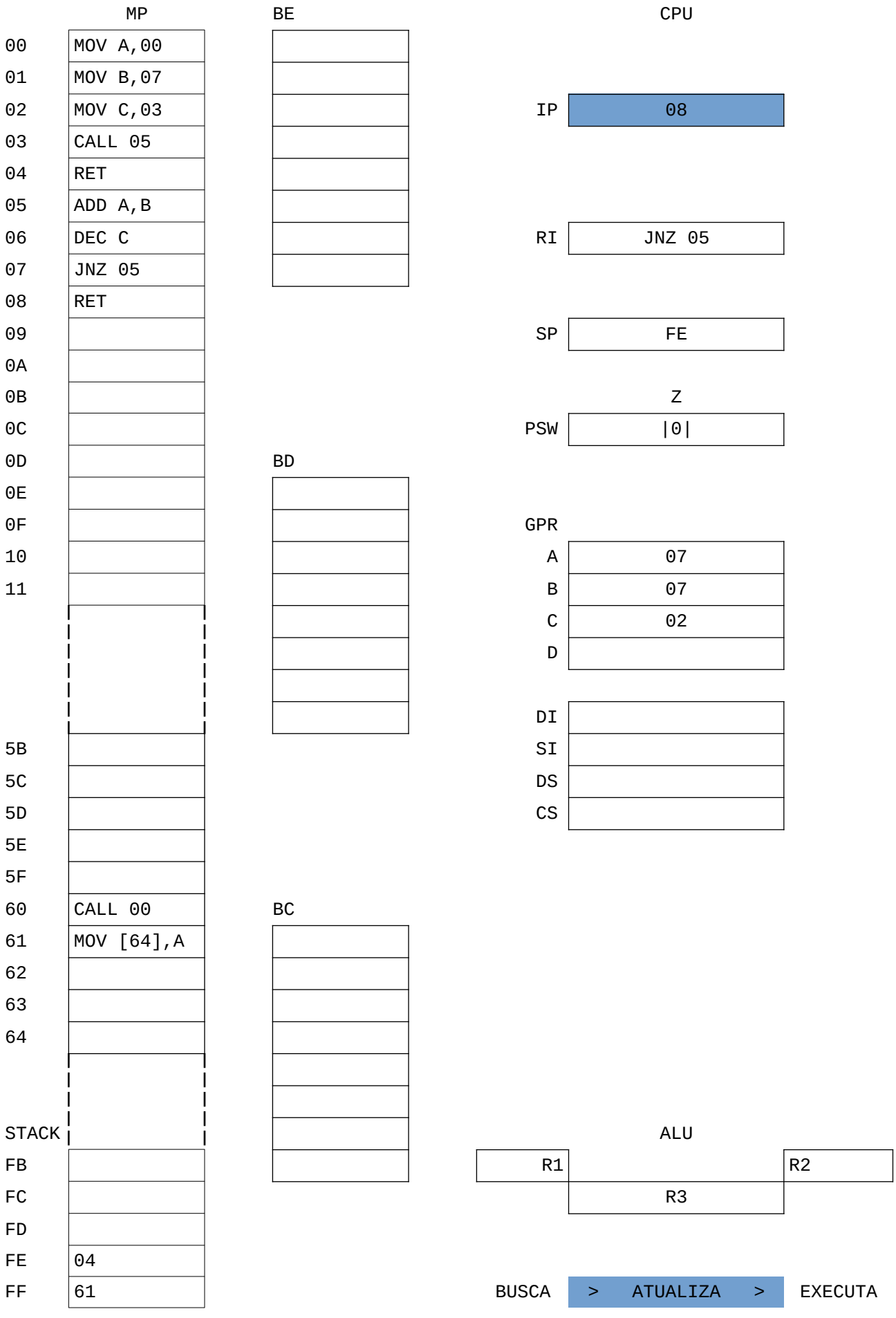
C 02
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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08 RET
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63
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STACK
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FC
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FE 04
FF 61
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BD
BC
CPU
IP 08
RI JNZ 05
SP FE
Z
PSW |0|
GPR
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C 02
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SI
DS
CS
ALU
R1
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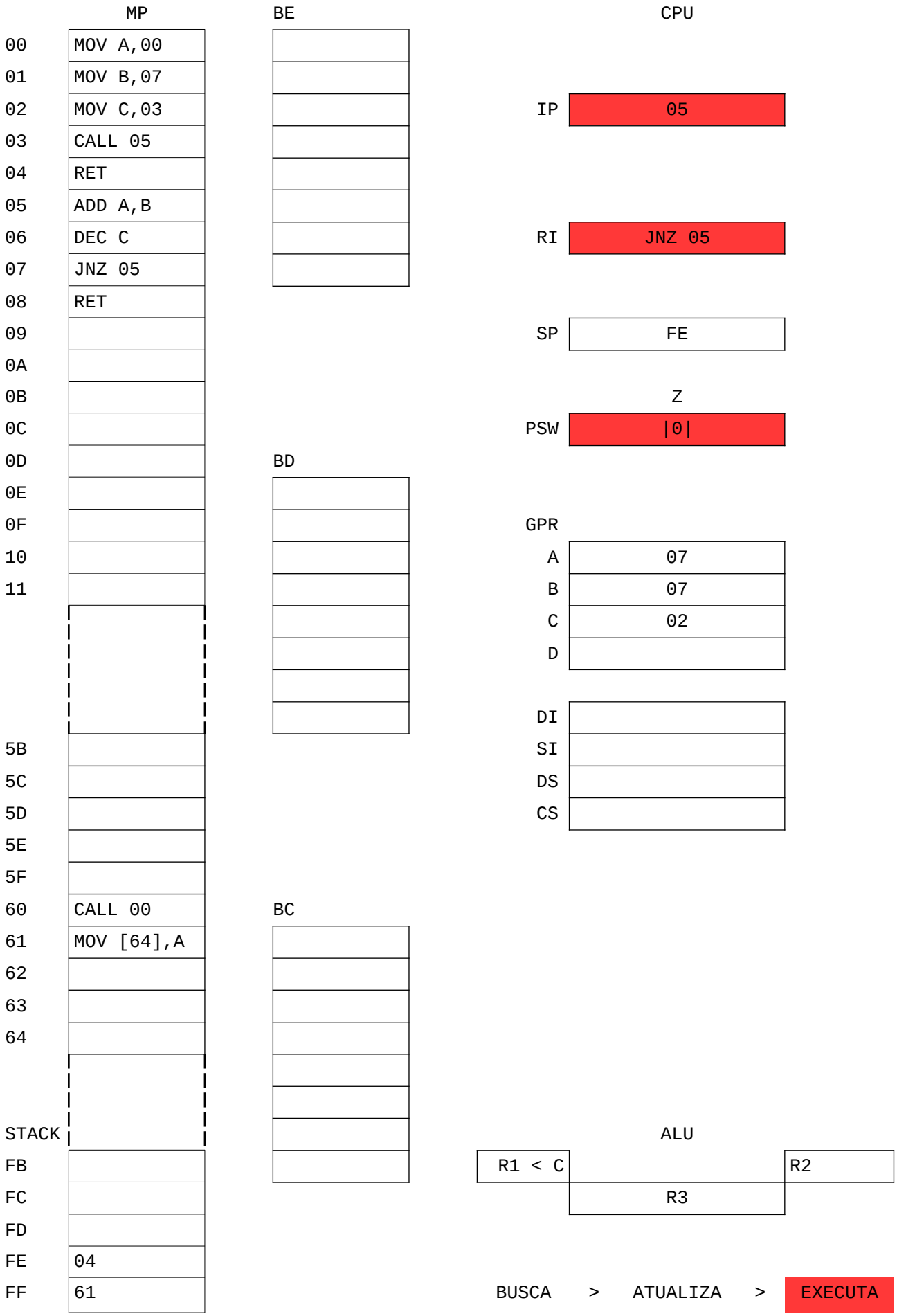
R3
BUSCA > ATUALIZA > EXECUTA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06 DEC C
07 JNZ 05
08 RET
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FE 04
FF 61
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IP 05
RI JNZ 05
SP FE
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PSW |0|
GPR
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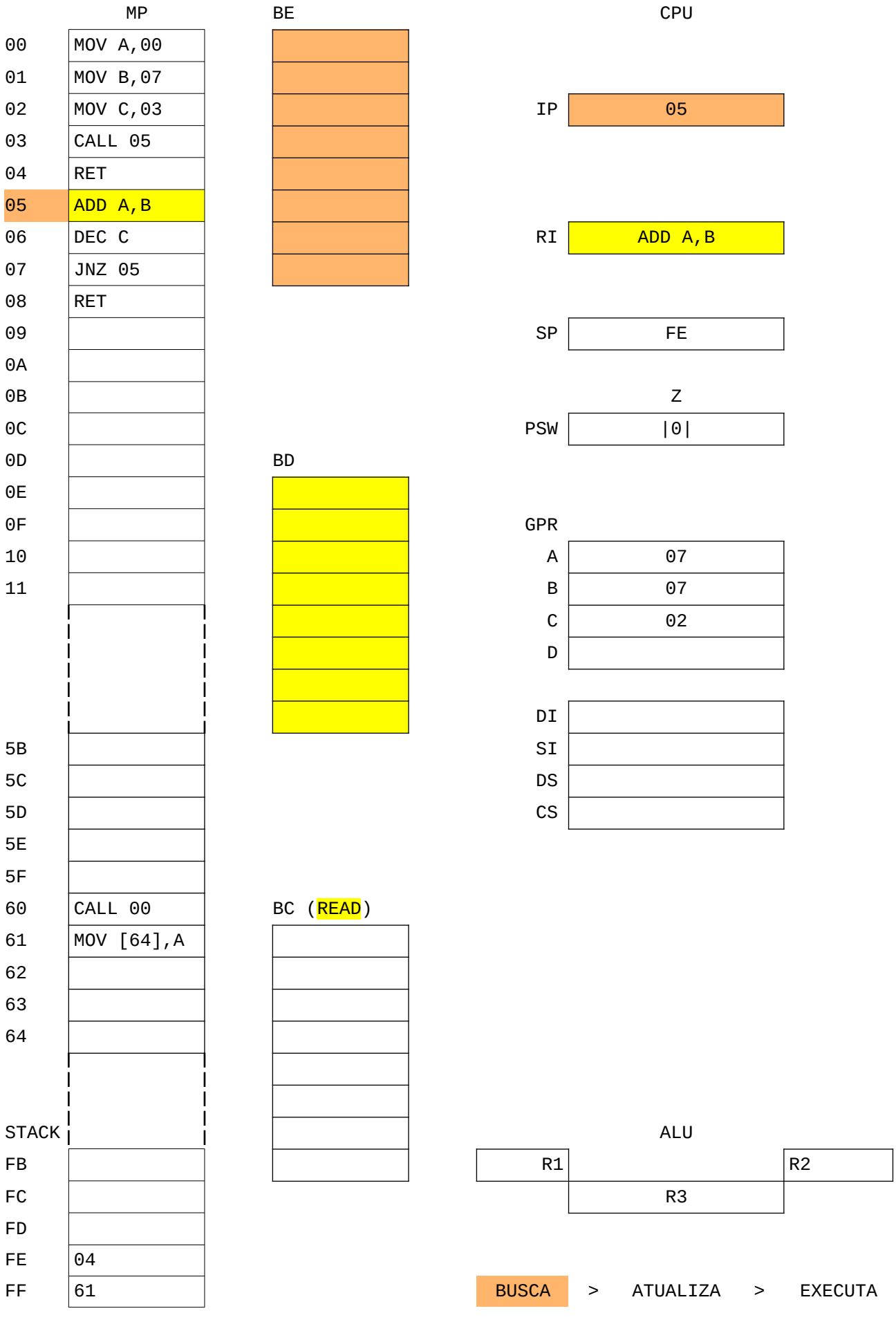
B 07
C 02
D
DI
SI
DS
CS
ALU
R1 < C
R2
R3
BUSCA > ATUALIZA > EXECUTA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06 DEC C
07 JNZ 05
08 RET
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STACK
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FC
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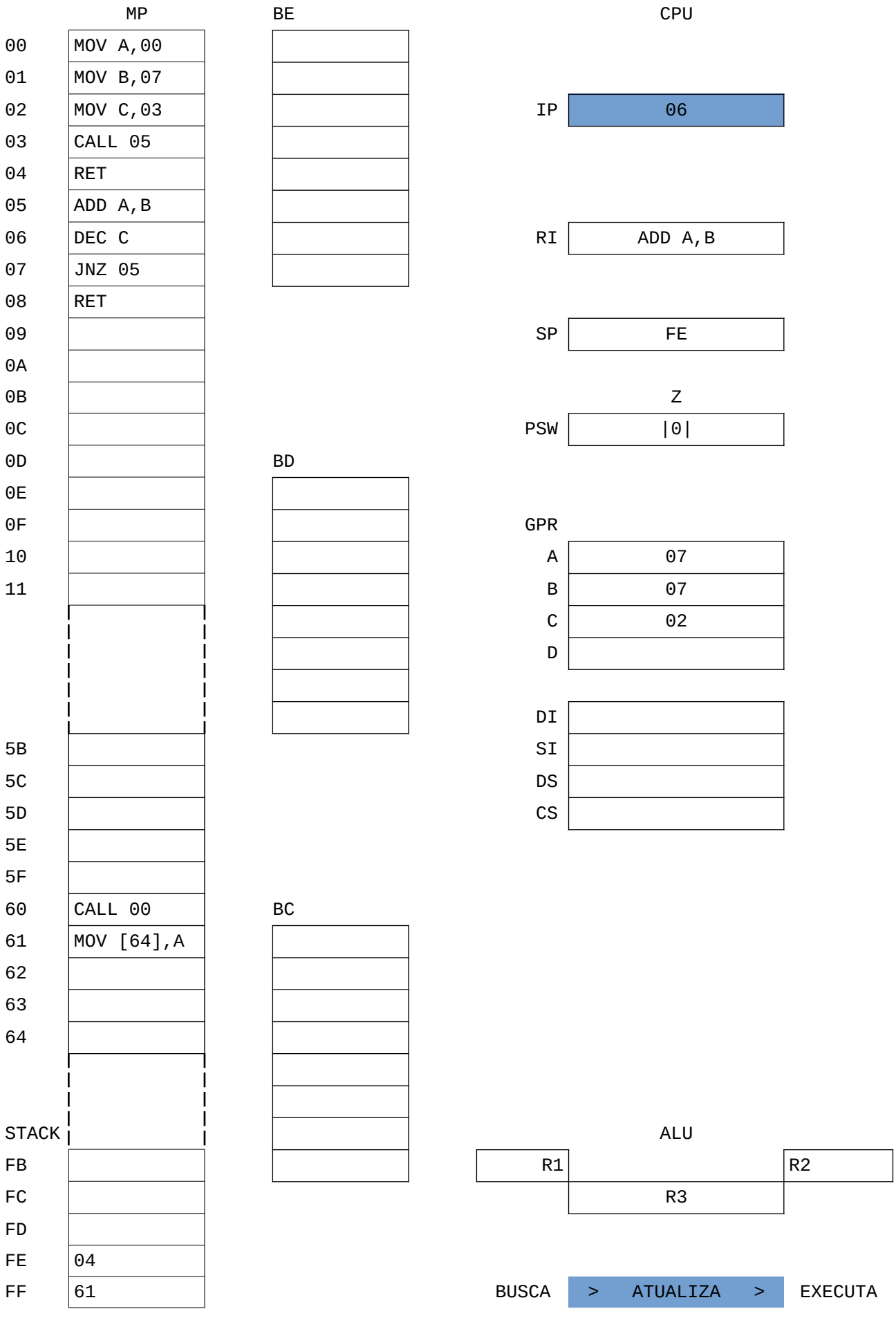
FE 04
FF 61
IP 05
RI ADD A,B
SP FE
Z
PSW |0|
BD
GPR
A 07
B 07
C 02
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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08 RET
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BD
BC
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
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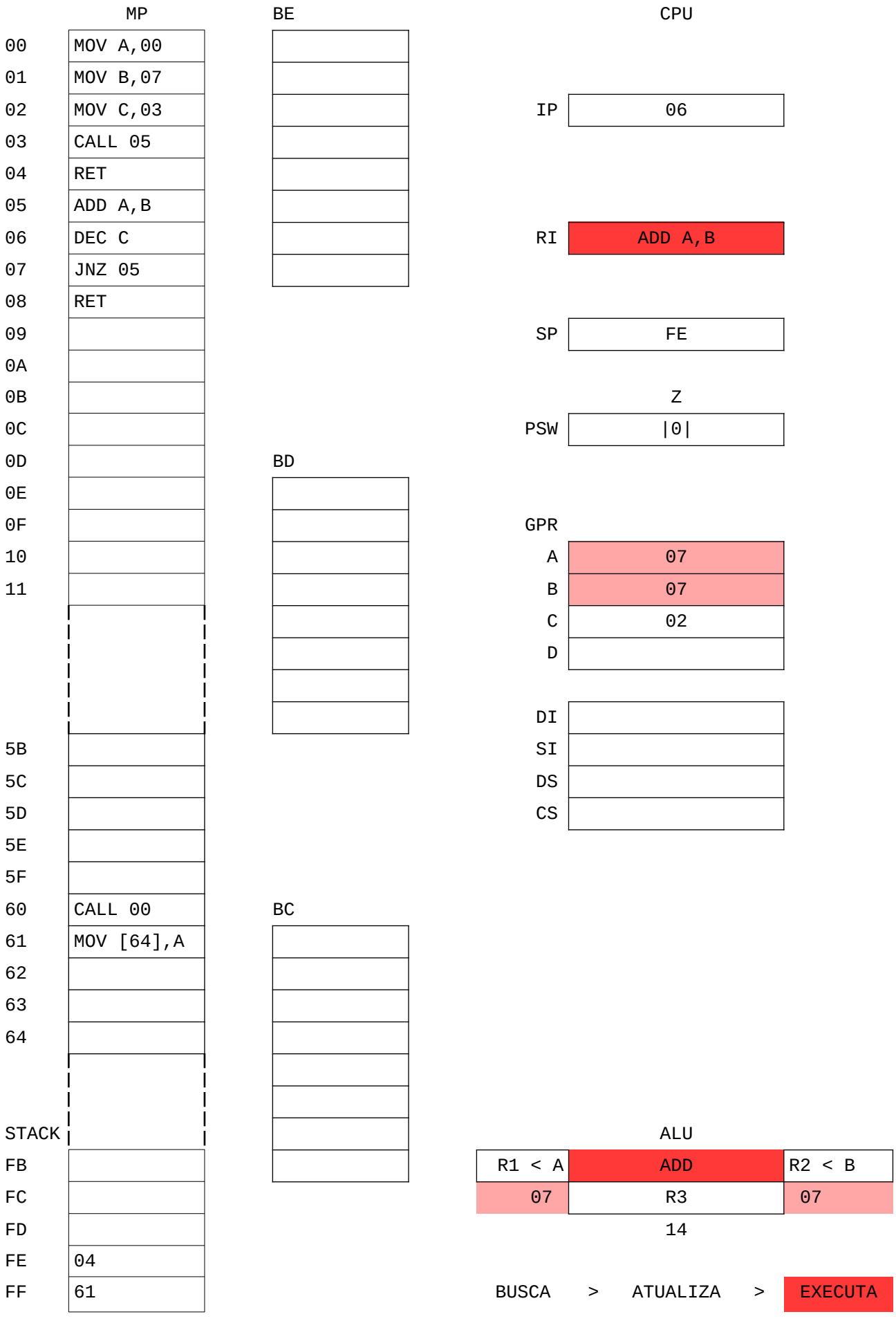
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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07 JNZ 05
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62
63
64
STACK
FB
FC
FD
FE 04
FF 61
BD
BC
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
GPR
A 07
B 07
C 02
D
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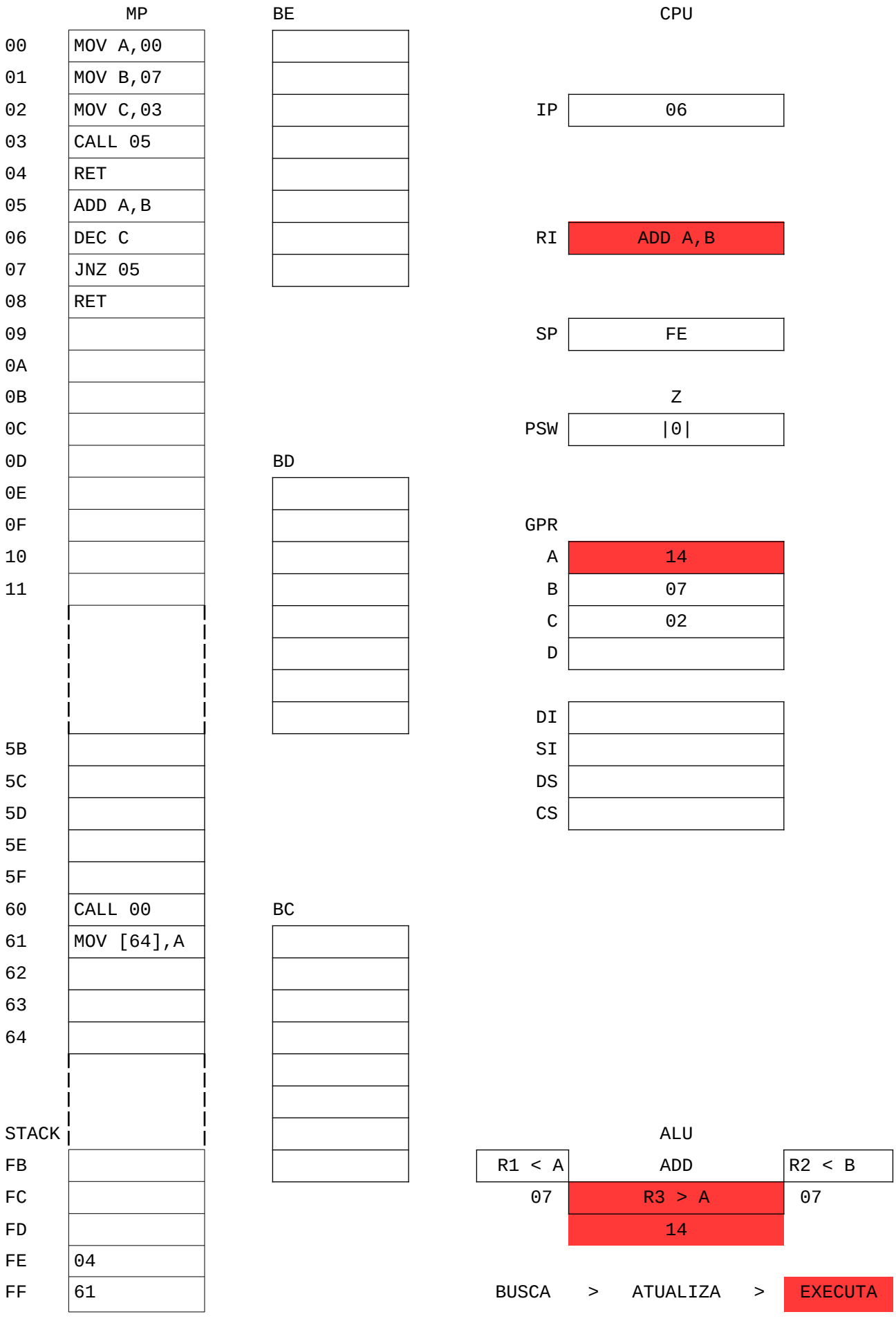
SI
DS
CS
ALU
R1 < A
ADD
R2 < B
07
R3
07
14
BUSCA > ATUALIZA > EXECUTA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02 MOV C,03
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04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
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11
5B
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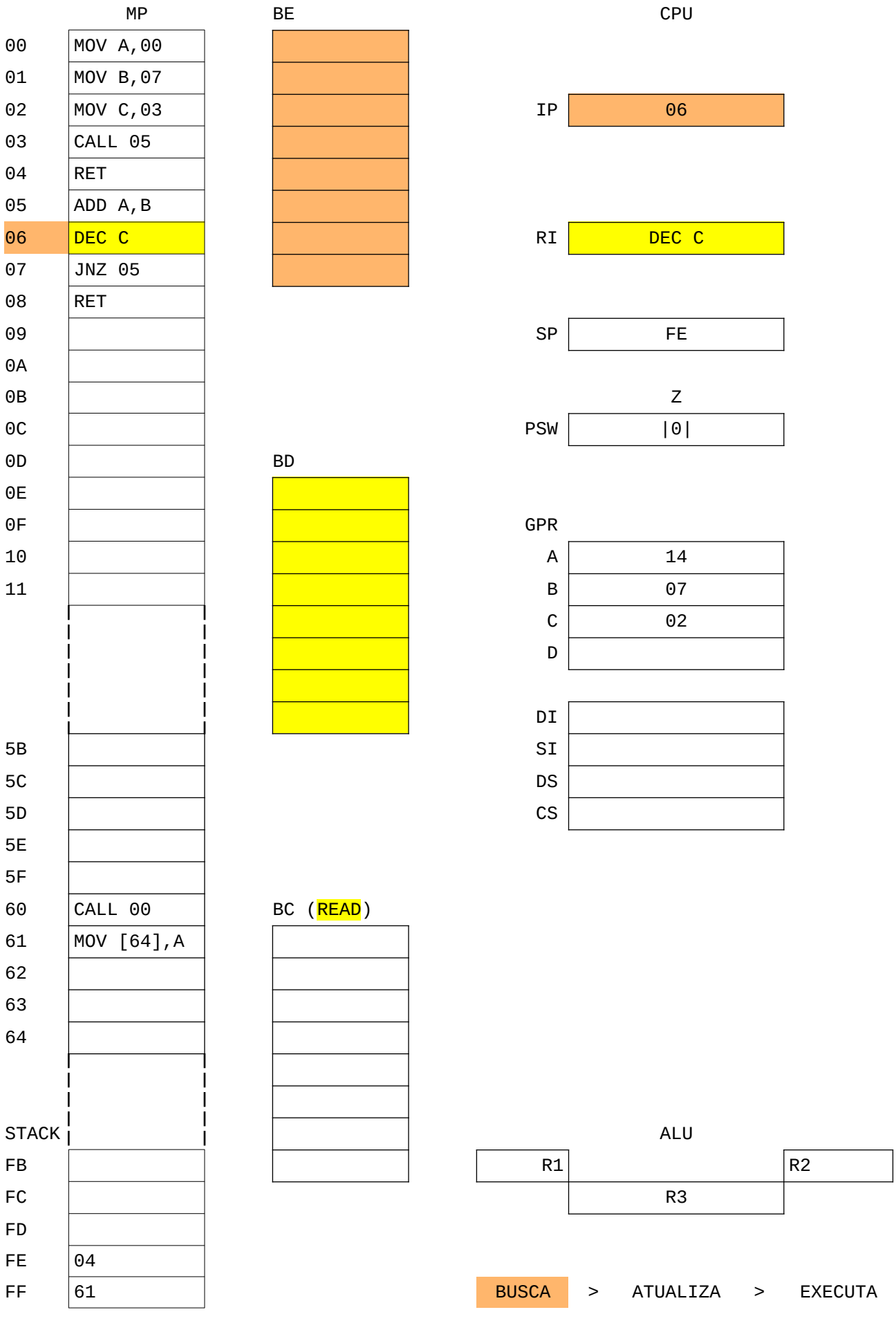
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
BD
BC
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
GPR
A 14
B 07
C 02
D
DI
SI
DS
CS
ALU
R1 < A
ADD
R2 < B
07
R3 > A
07
14
BUSCA > ATUALIZA > EXECUTA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06 DEC C
07 JNZ 05
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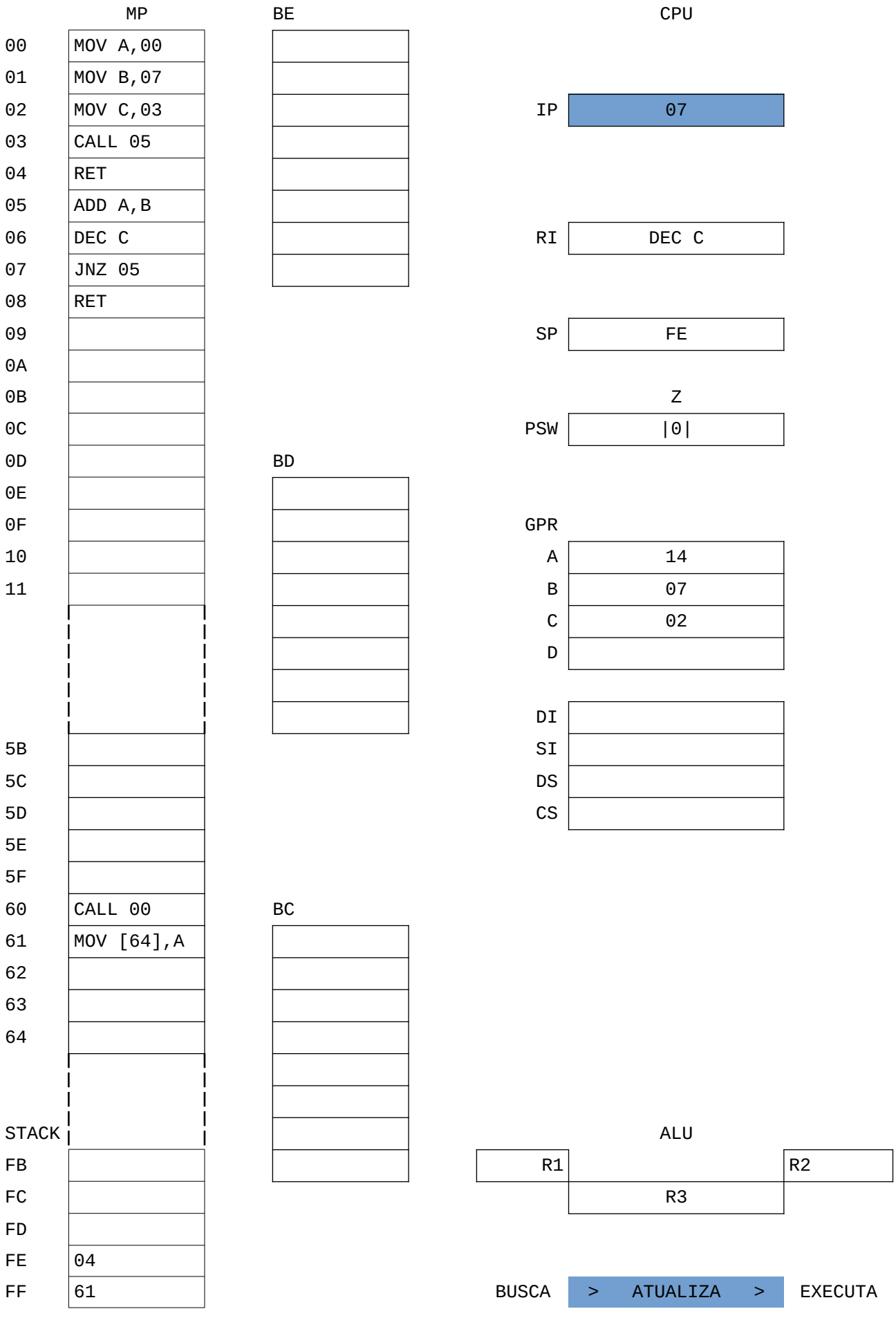
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 06
RI DEC C
SP FE
Z
PSW |0|
BD
GPR
A 14
B 07
C 02
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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Z
PSW |0|
GPR
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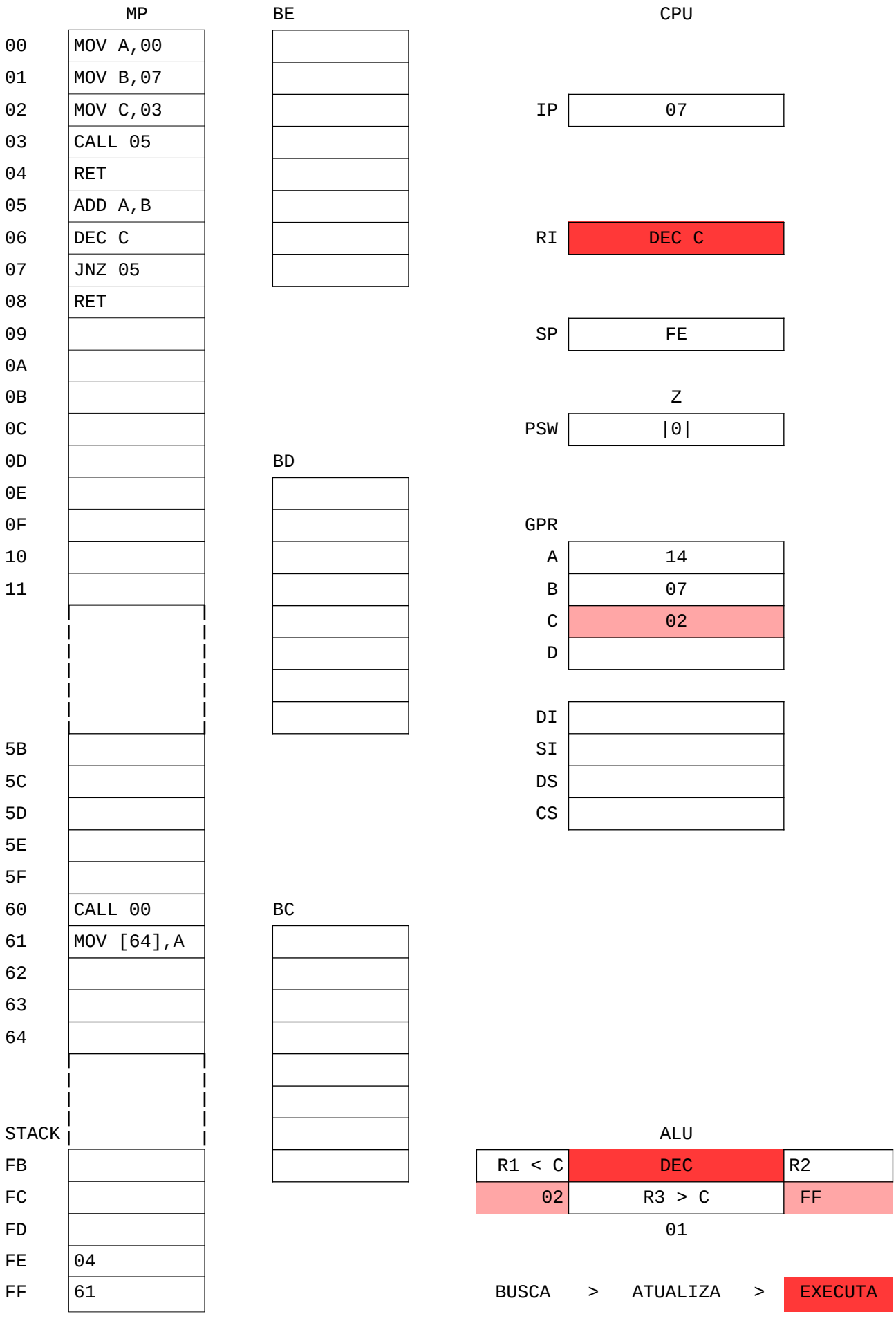
C 02
D
DI
SI
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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FE 04
FF 61
BD
BC
IP 07
RI DEC C
SP FE
Z
PSW |0|
GPR
A 14
B 07
C 02
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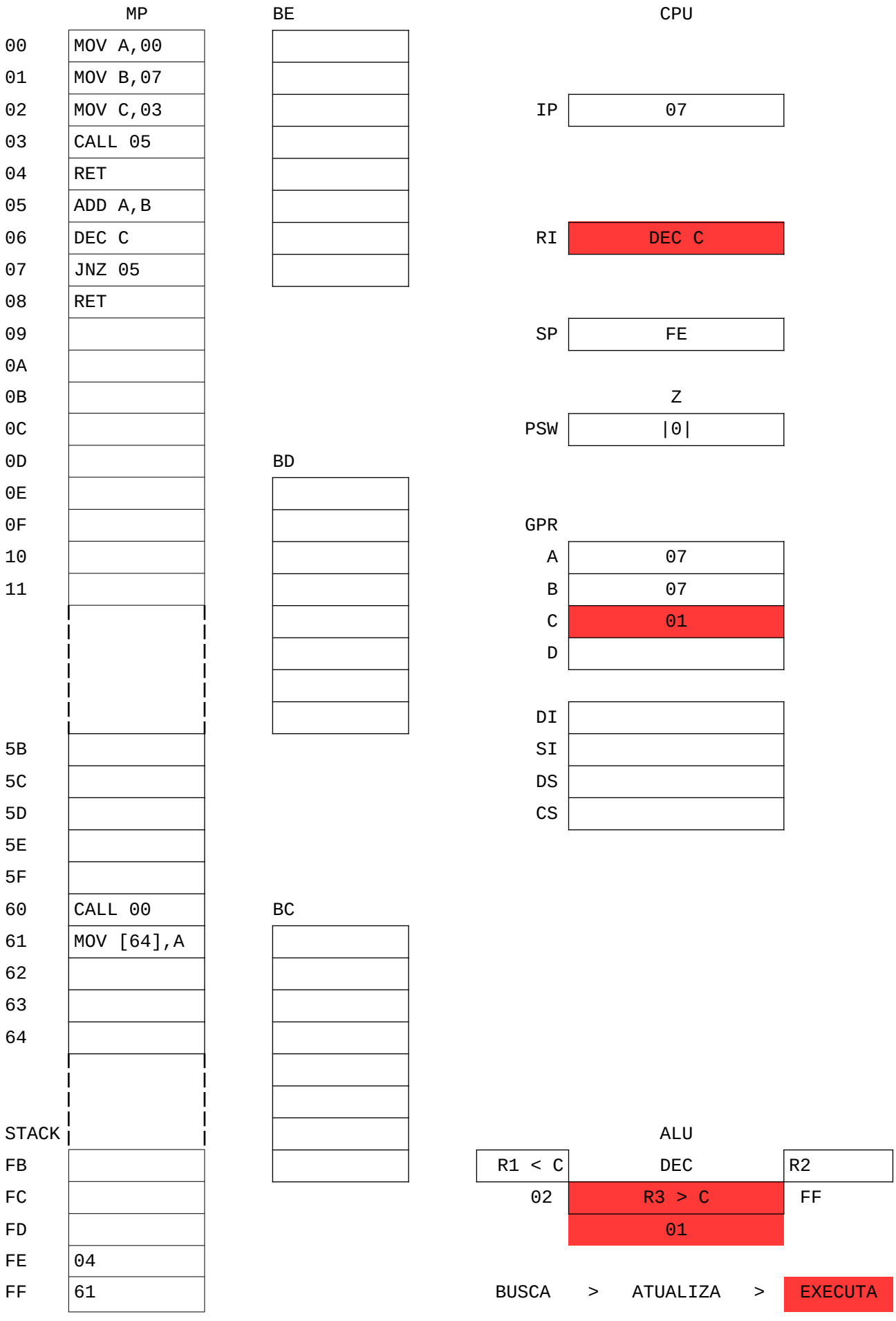
CS
ALU
R1 < C
DEC
R2
02
R3 > C
FF
01
BUSCA > ATUALIZA > EXECUTA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FF 61
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BC
IP 07
RI DEC C
SP FE
Z
PSW |0|
GPR
A 07
B 07
C 01
D
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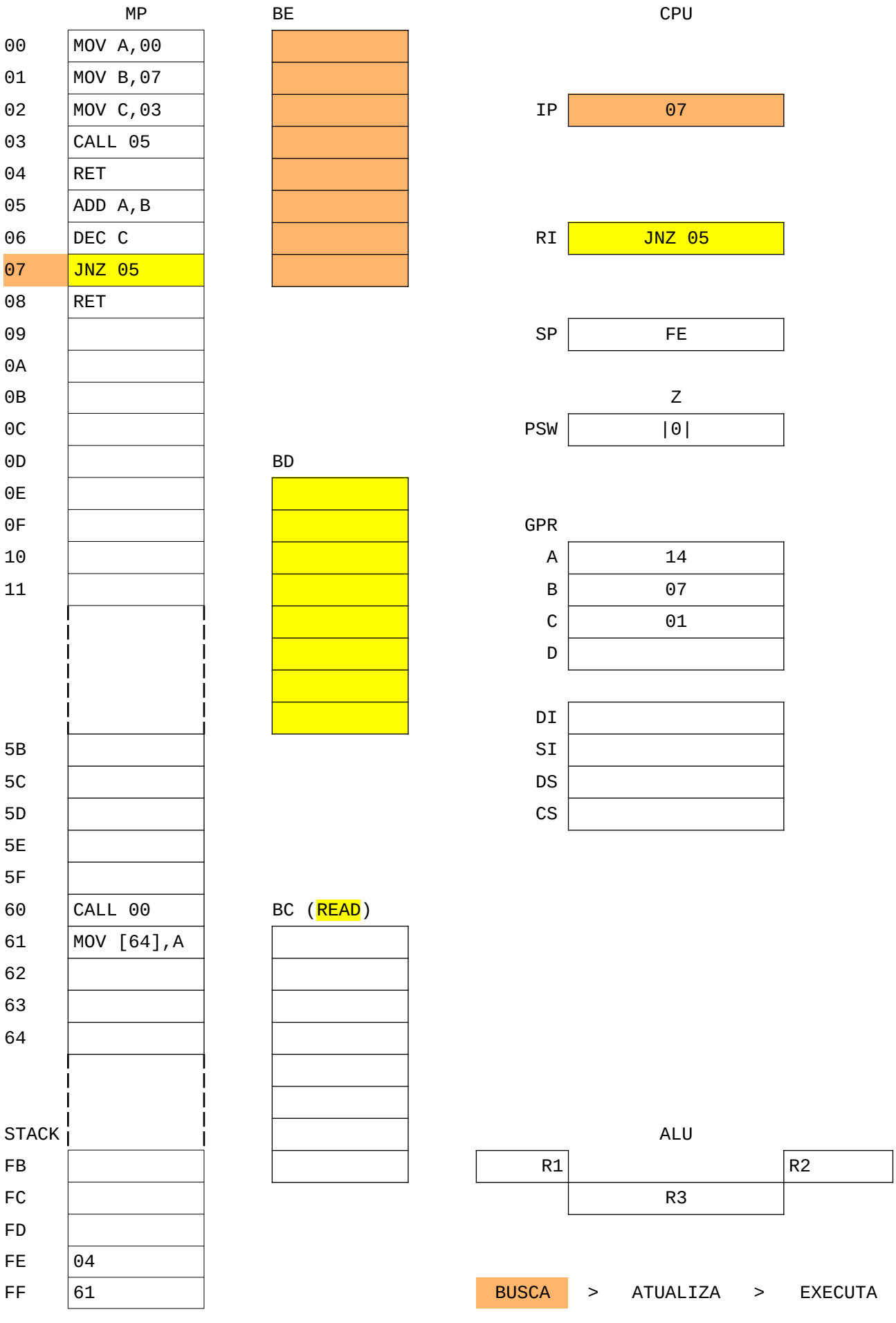
ALU
R1 < C
DEC
R2
02
R3 > C
FF
01
BUSCA > ATUALIZA > EXECUTA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STACK
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FF 61
IP 07
RI JNZ 05
SP FE
Z
PSW |0|
BD
GPR
A 14
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C 01
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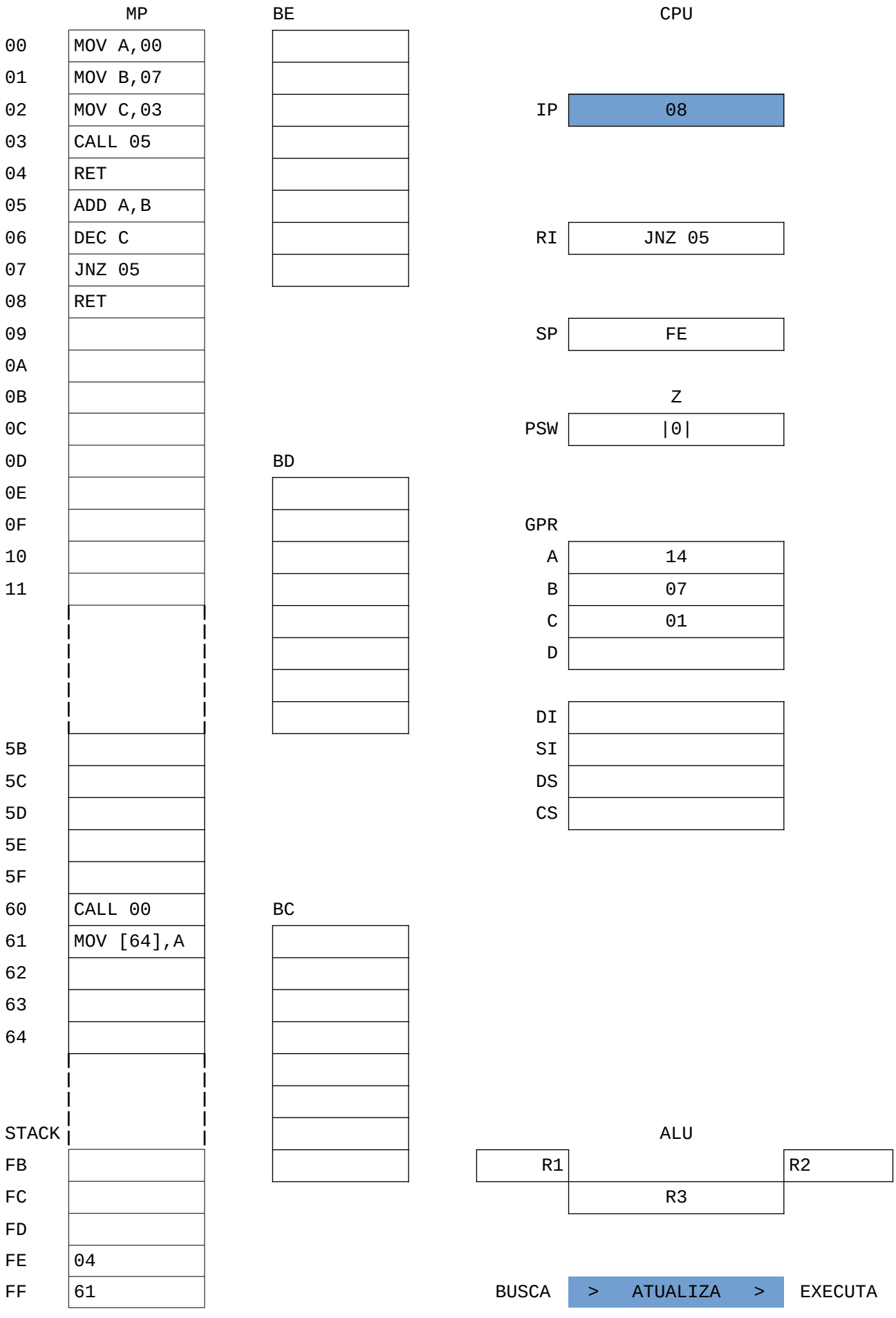
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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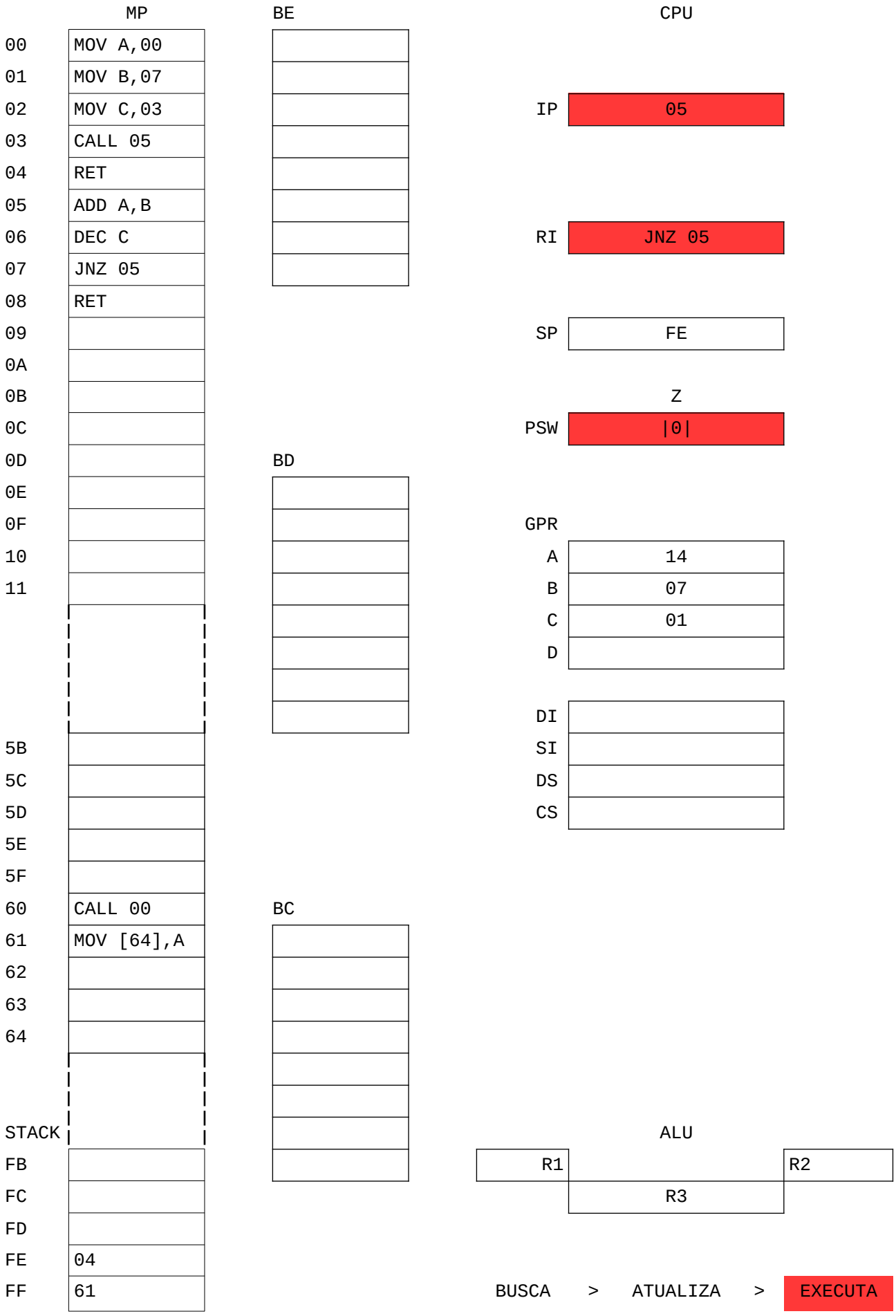
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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PSW |0|
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ALU
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R3
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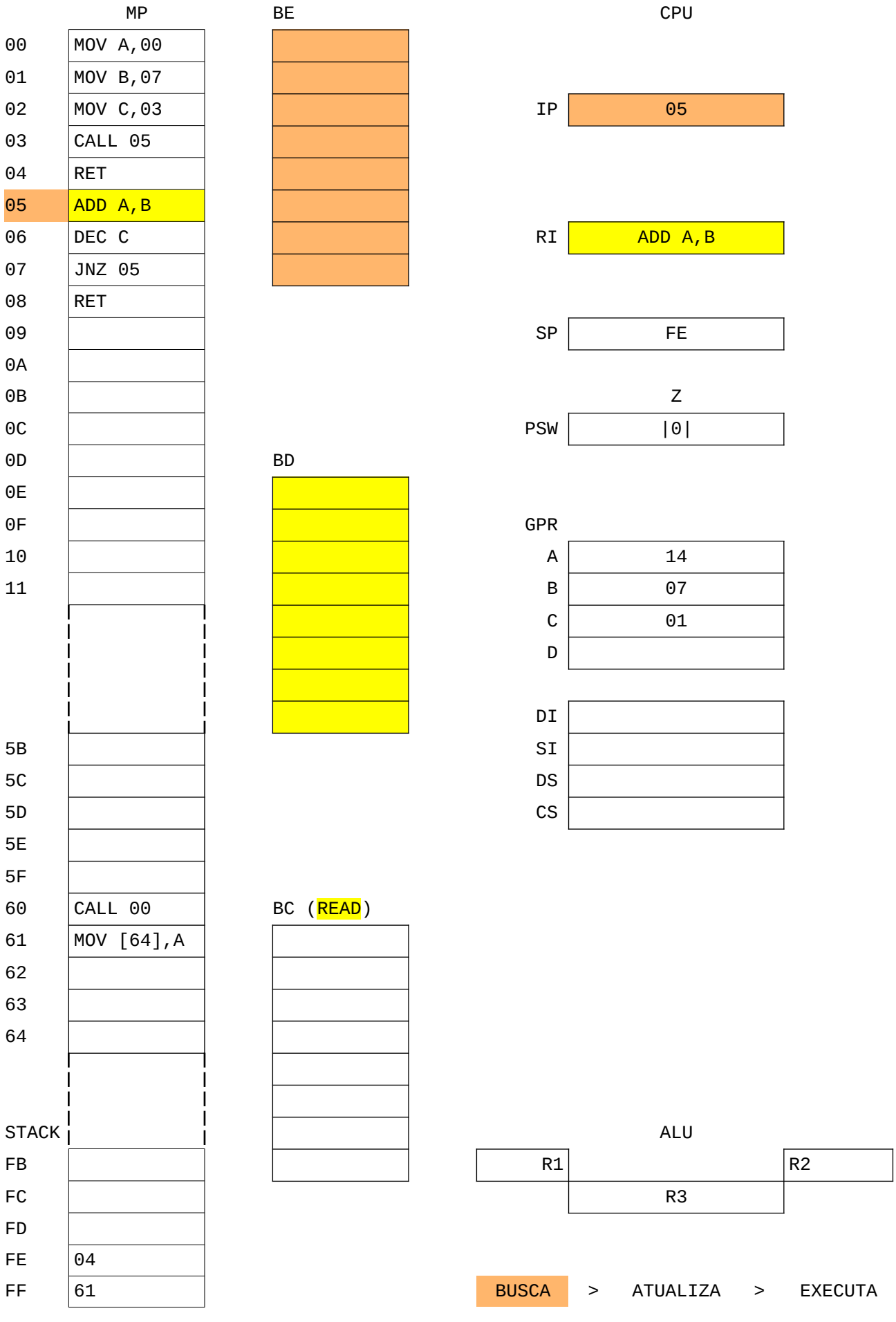
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63
64
STACK
FB
FC
FD
FE 04
FF 61
BE
BD
BC (READ)
CPU
IP 05
RI ADD A,B
SP FE
Z
PSW |0|
GPR
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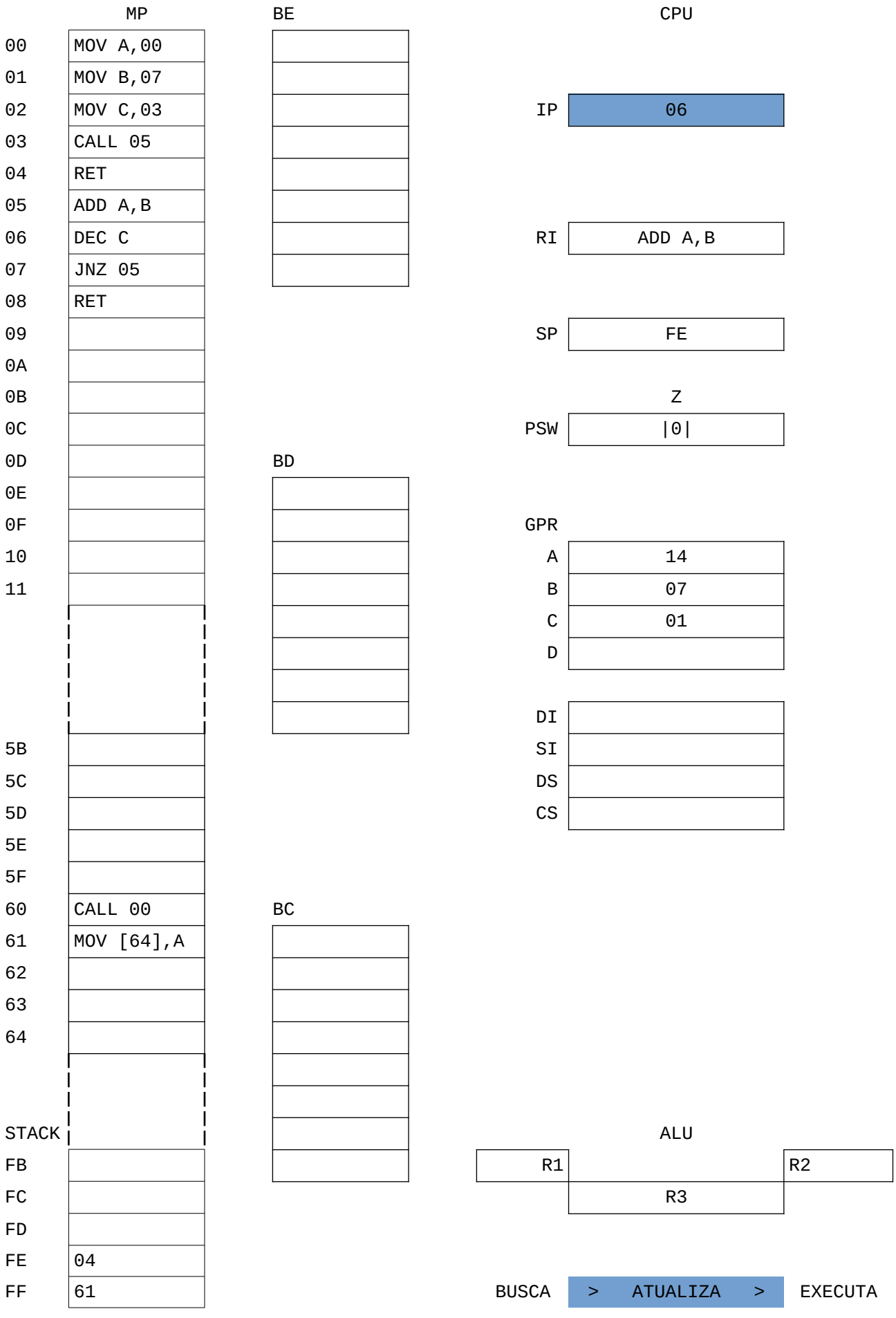
DS
CS
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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RI ADD A,B
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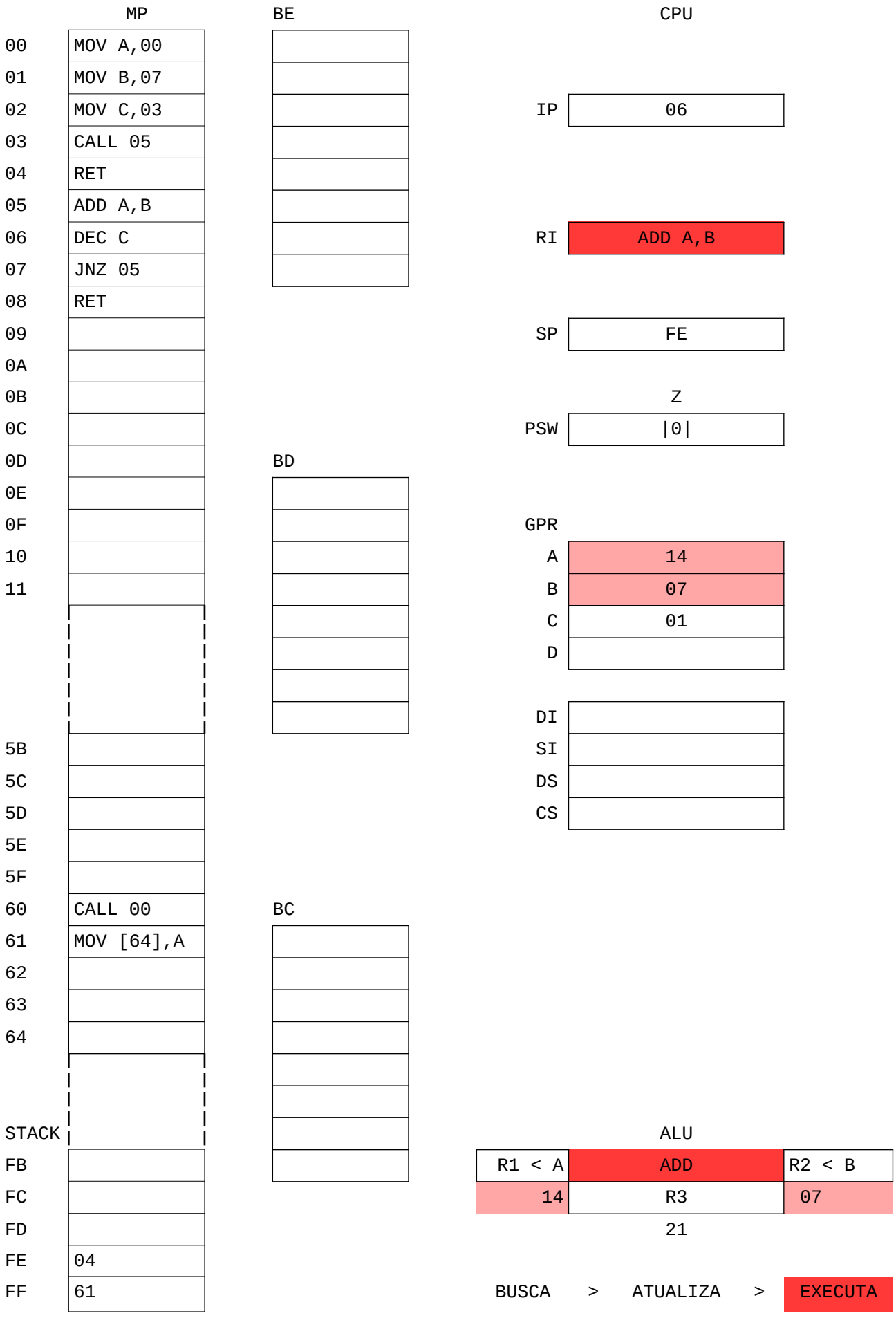
R2
R3
BUSCA > ATUALIZA > EXECUTA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02 MOV C,03
03 CALL 05
04 RET
05 ADD A,B
06 DEC C
07 JNZ 05
08 RET
09
0A
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0F
10
11
5B
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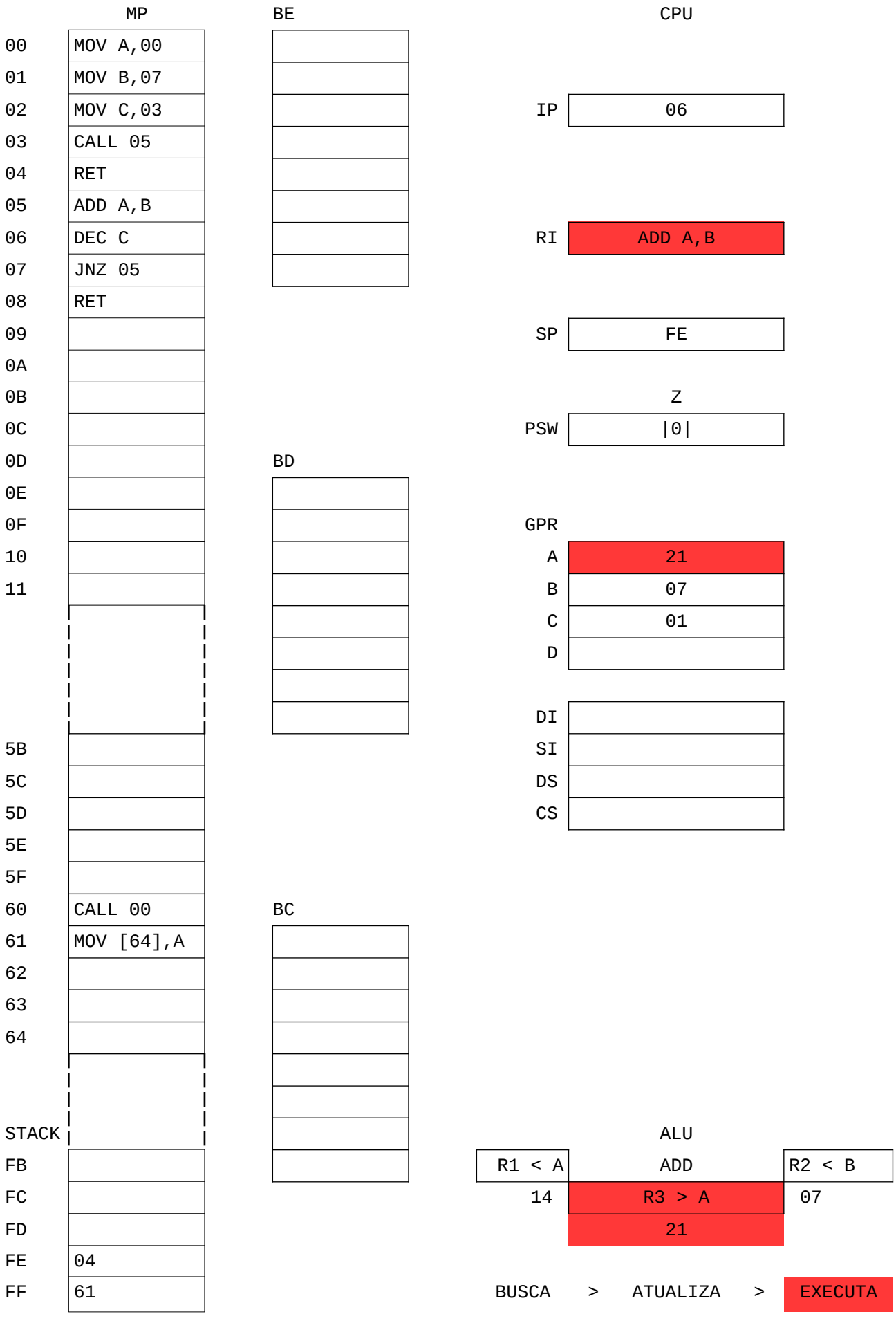
5E
5F
60 CALL 00
61 MOV [64],A
62
63
64
STACK
FB
FC
FD
FE 04
FF 61
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
BD
GPR
A 14
B 07
C 01
D
DI
SI
DS
CS
BC
ALU
R1 < A
ADD
R2 < B
14
R3
07
21
BUSCA > ATUALIZA > EXECUTA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07 JNZ 05
08 RET
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60 CALL 00
61 MOV [64],A
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STACK
FB
FC
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FE 04
FF 61
IP 06
RI ADD A,B
SP FE
Z
PSW |0|
BD
GPR
A 21
B 07
C 01
D
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SI
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ALU
R1 < A
ADD
R2 < B
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R3 > A
07
21
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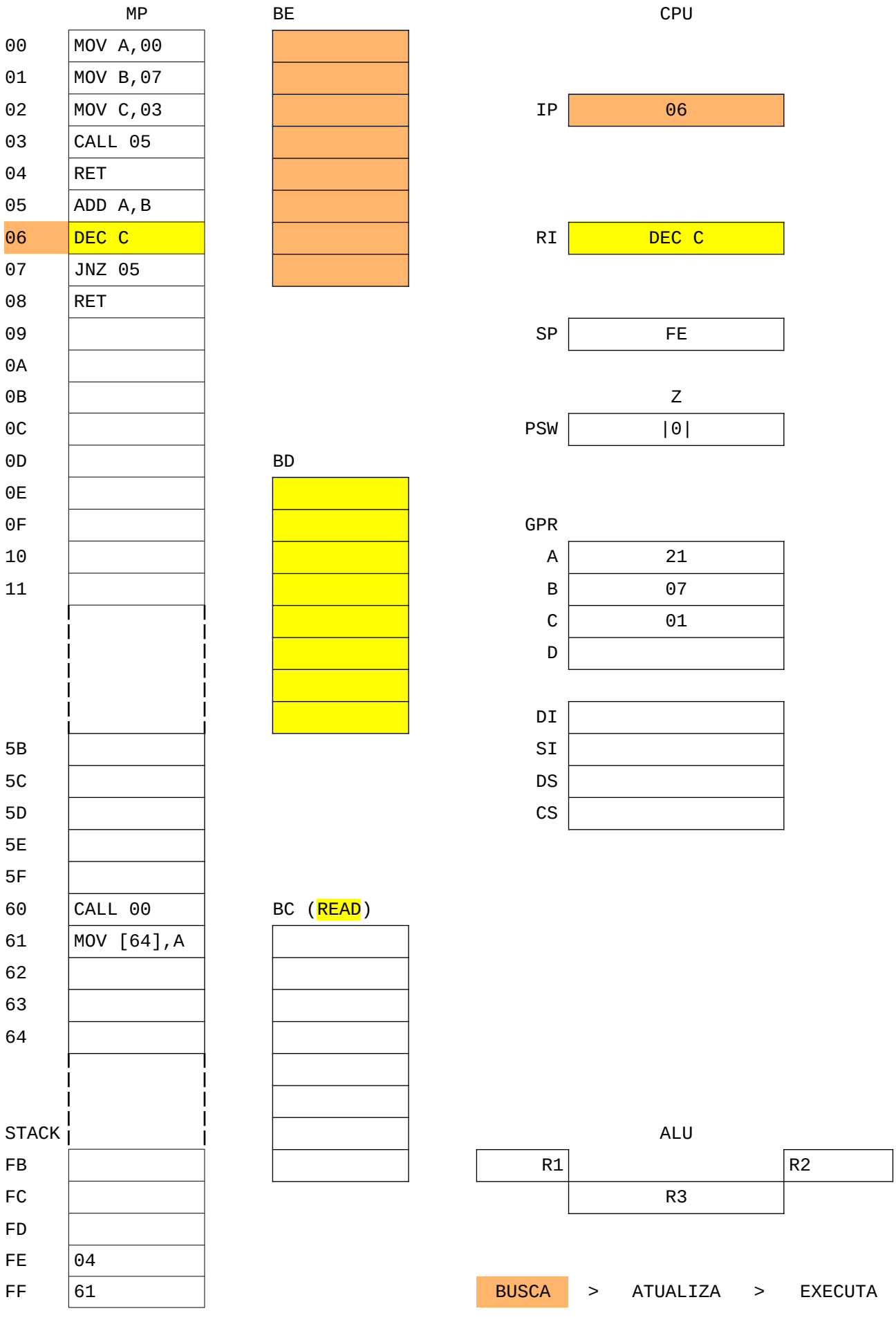
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08 RET
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60 CALL 00
61 MOV [64],A
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63
64
STACK
FB
FC
FD
FE 04
FF 61
BE
BD
BC (READ)
CPU
IP 06
RI DEC C
SP FE
Z
PSW |0|
GPR
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C 01
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DI
SI
DS
CS
ALU
R1
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R3
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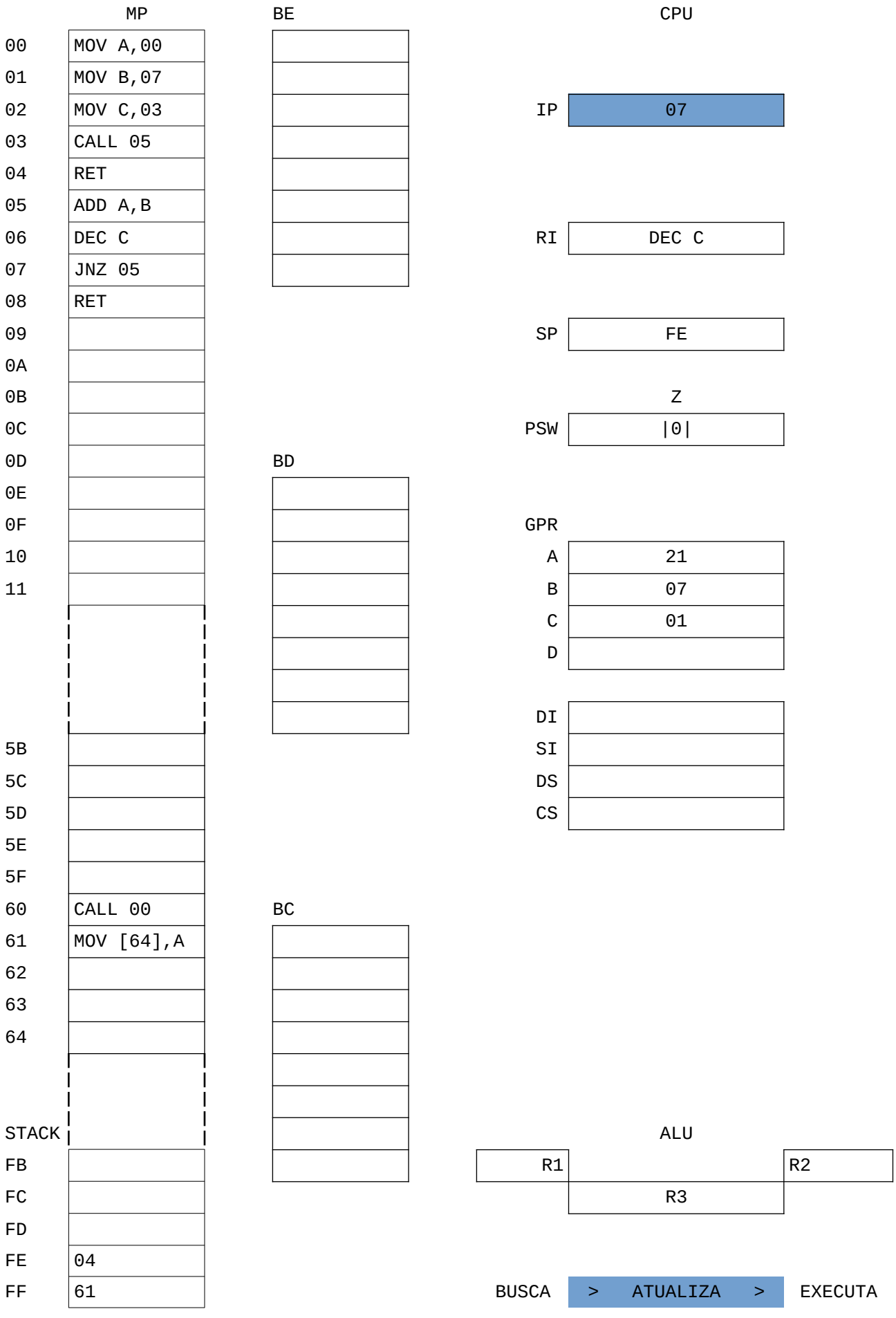
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60 CALL 00
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SP FE
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PSW |0|
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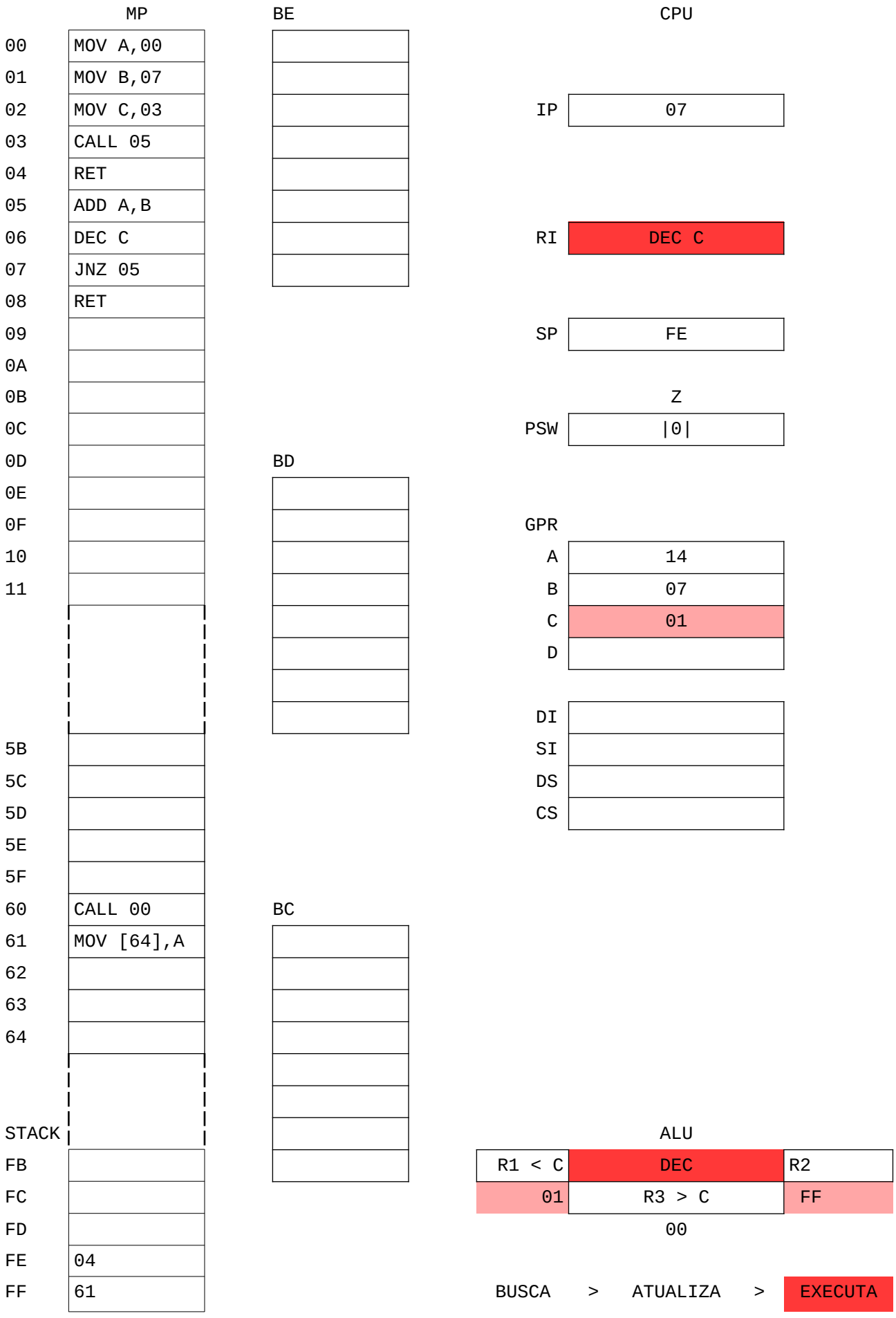
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FC
FD
FE 04
FF 61
IP 07
RI DEC C
SP FE
Z
PSW |0|
BD
GPR
A 14
B 07
C 01
D
DI
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BC
ALU
R1 < C
DEC
R2
01
R3 > C
FF
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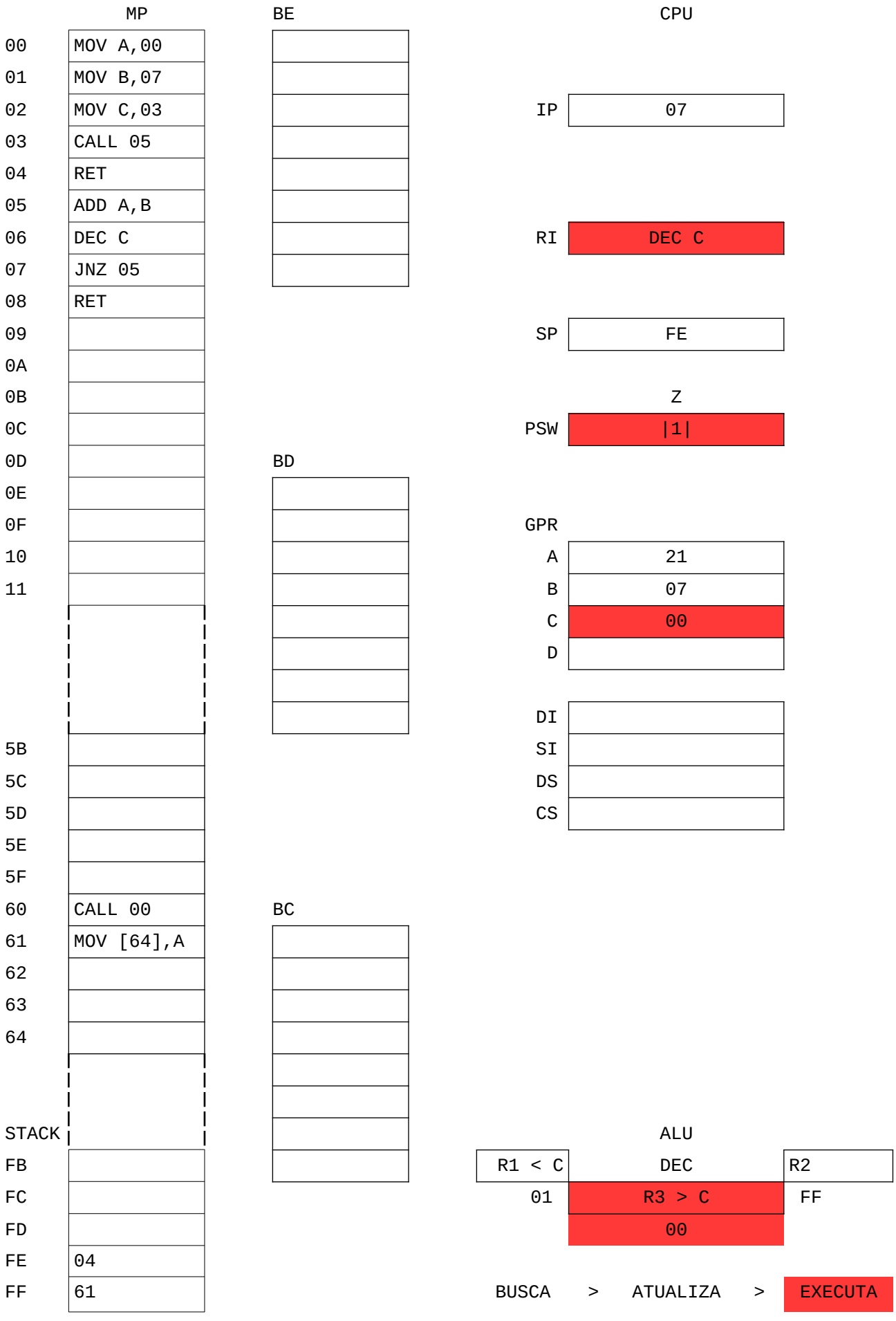
BUSCA > ATUALIZA > EXECUTA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STACK
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FD
FE 04
FF 61
IP 07
RI DEC C
SP FE
Z
PSW |1|
BD
GPR
A 21
B 07
C 00
D
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BC
ALU
R1 < C
DEC
R2
01
R3 > C
FF
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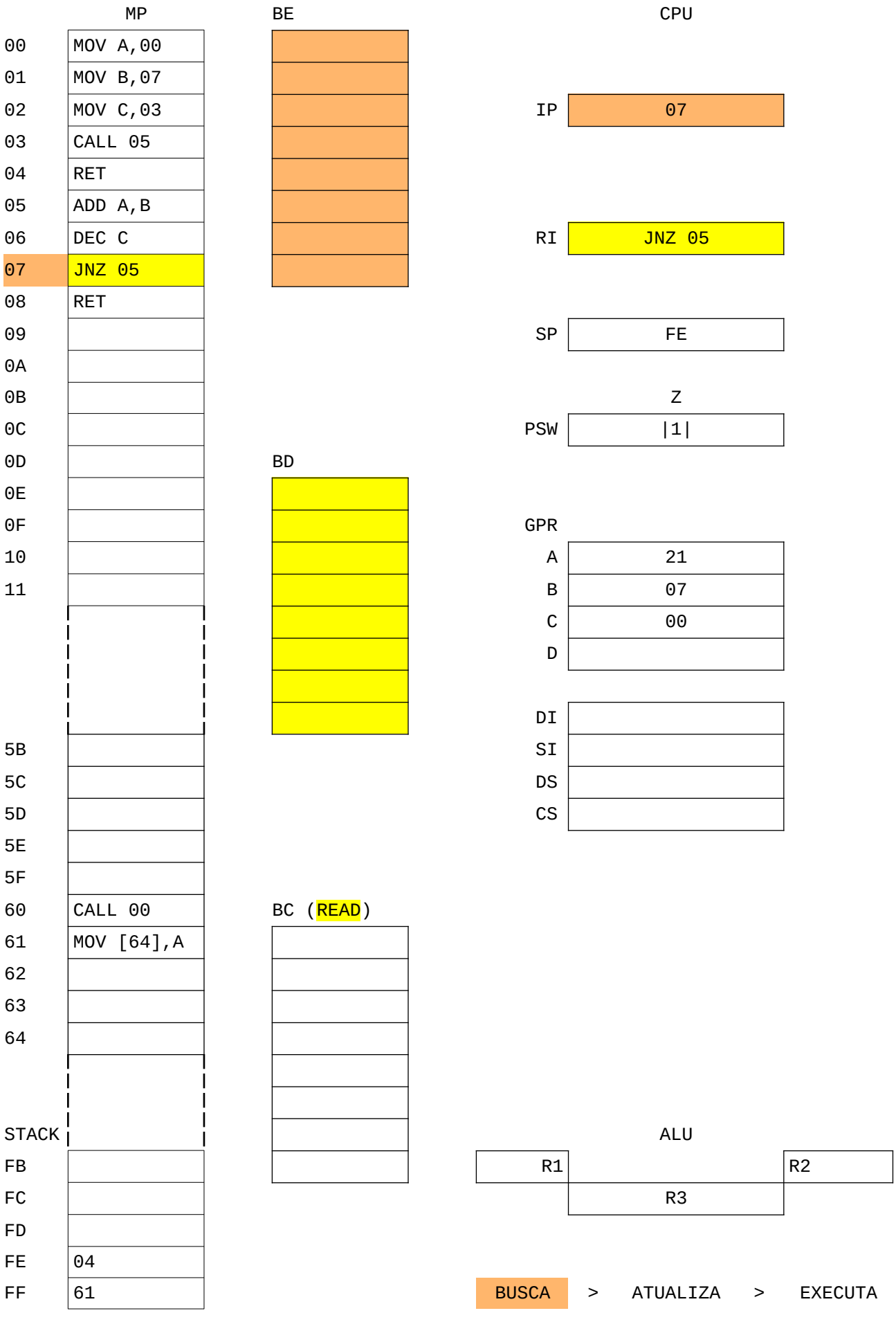
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04 RET
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06 DEC C
07 JNZ 05
08 RET
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FD
FE 04
FF 61
IP 07
RI JNZ 05
SP FE
Z
PSW |1|
BD
GPR
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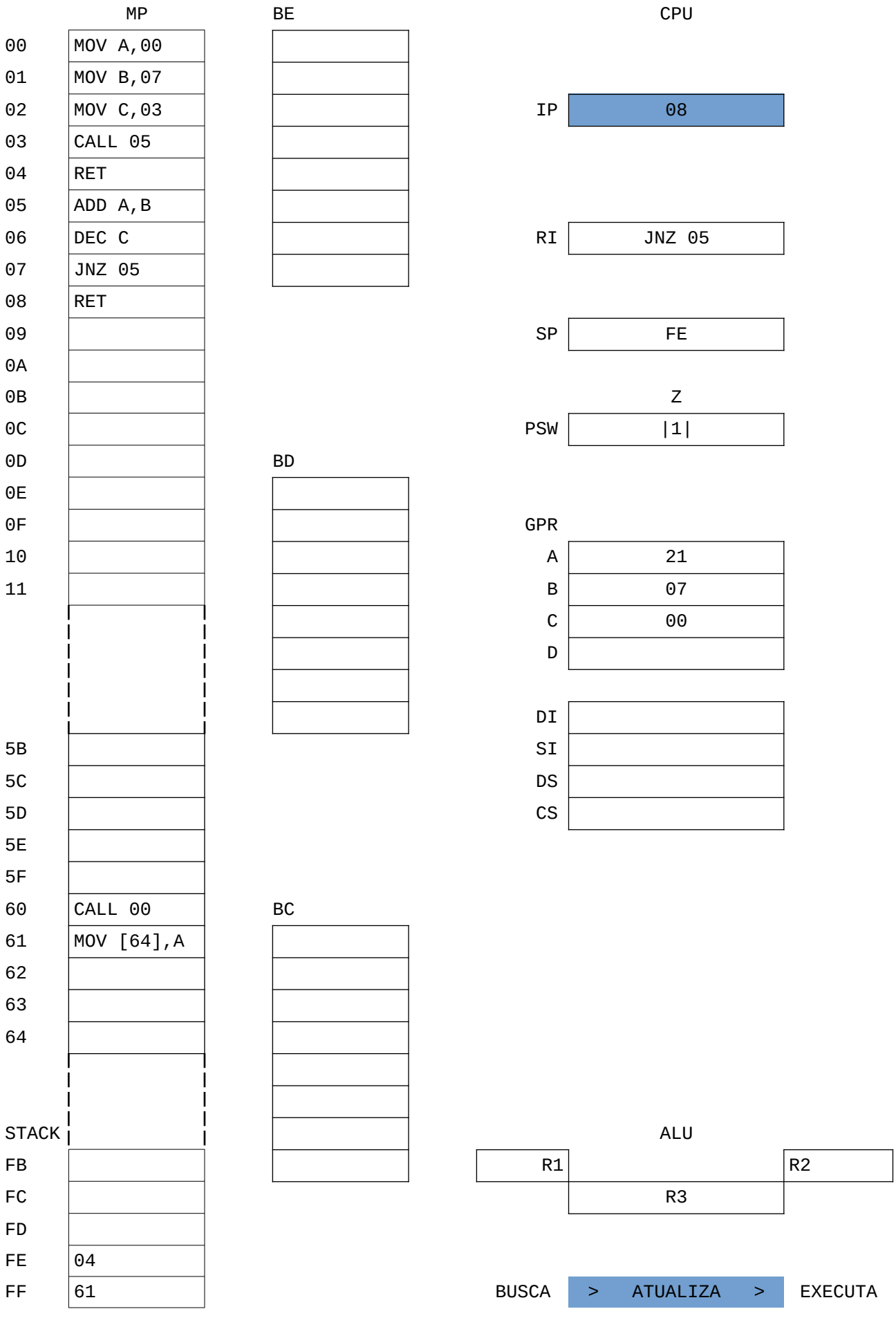
B 07
C 00
D
DI
SI
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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08 RET
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STACK
FB
FC
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FE 04
FF 61
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BD
BC
CPU
IP 08
RI JNZ 05
SP FE
Z
PSW |1|
GPR
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C 00
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DS
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ALU
R1
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R3
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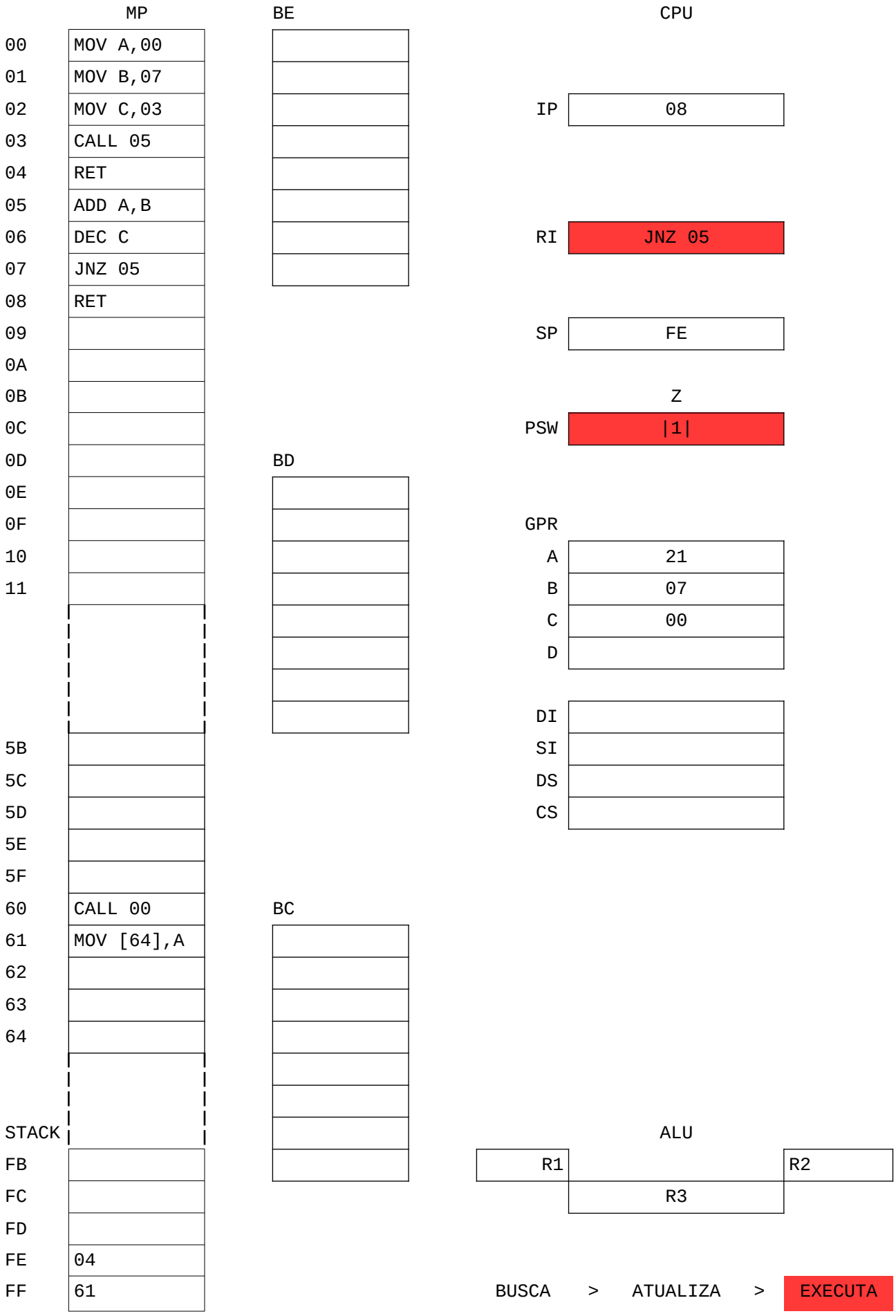
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RI JNZ 05
SP FE
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PSW |1|
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ALU
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R3
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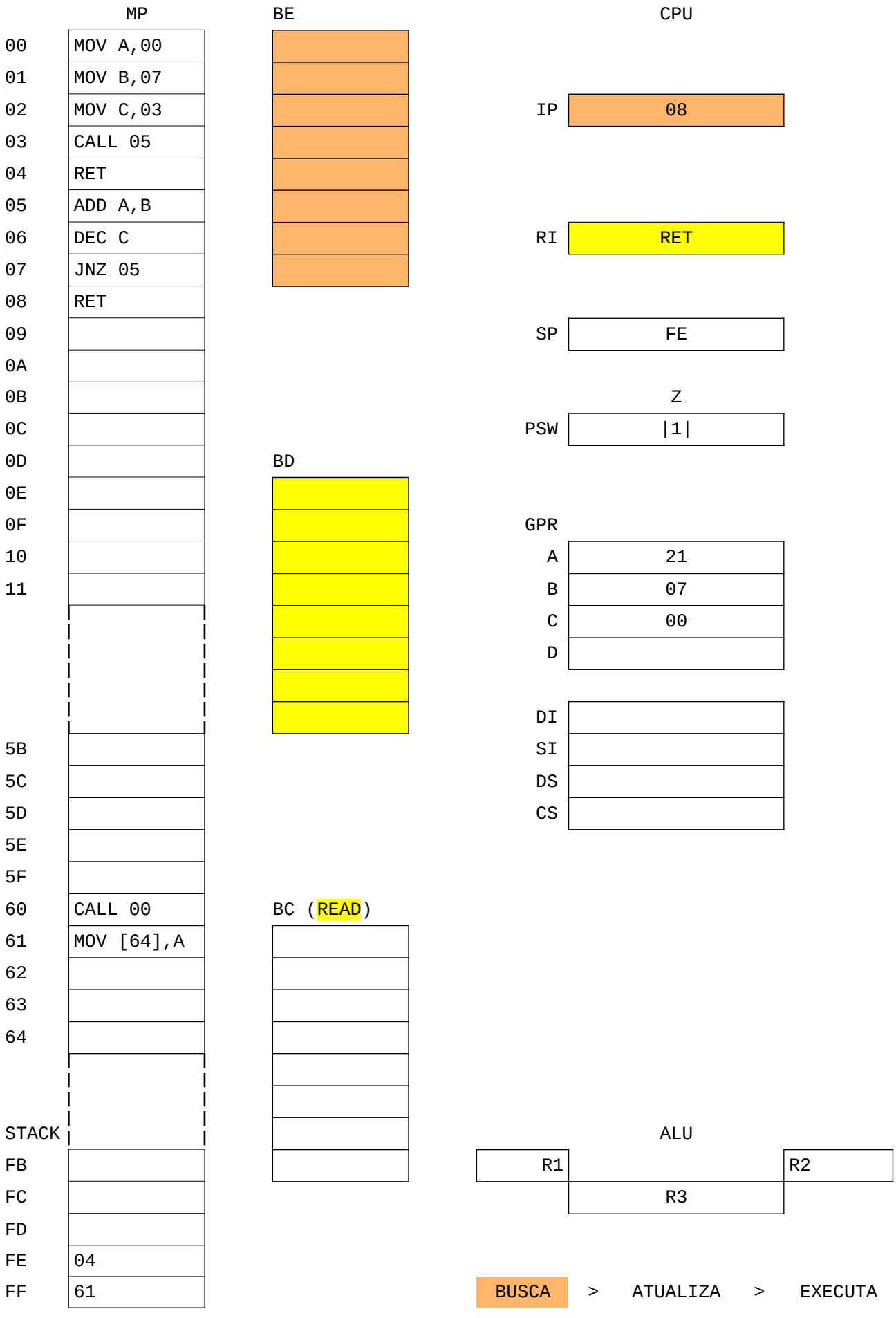
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PSW |1|
BD
GPR
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C 00
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DI
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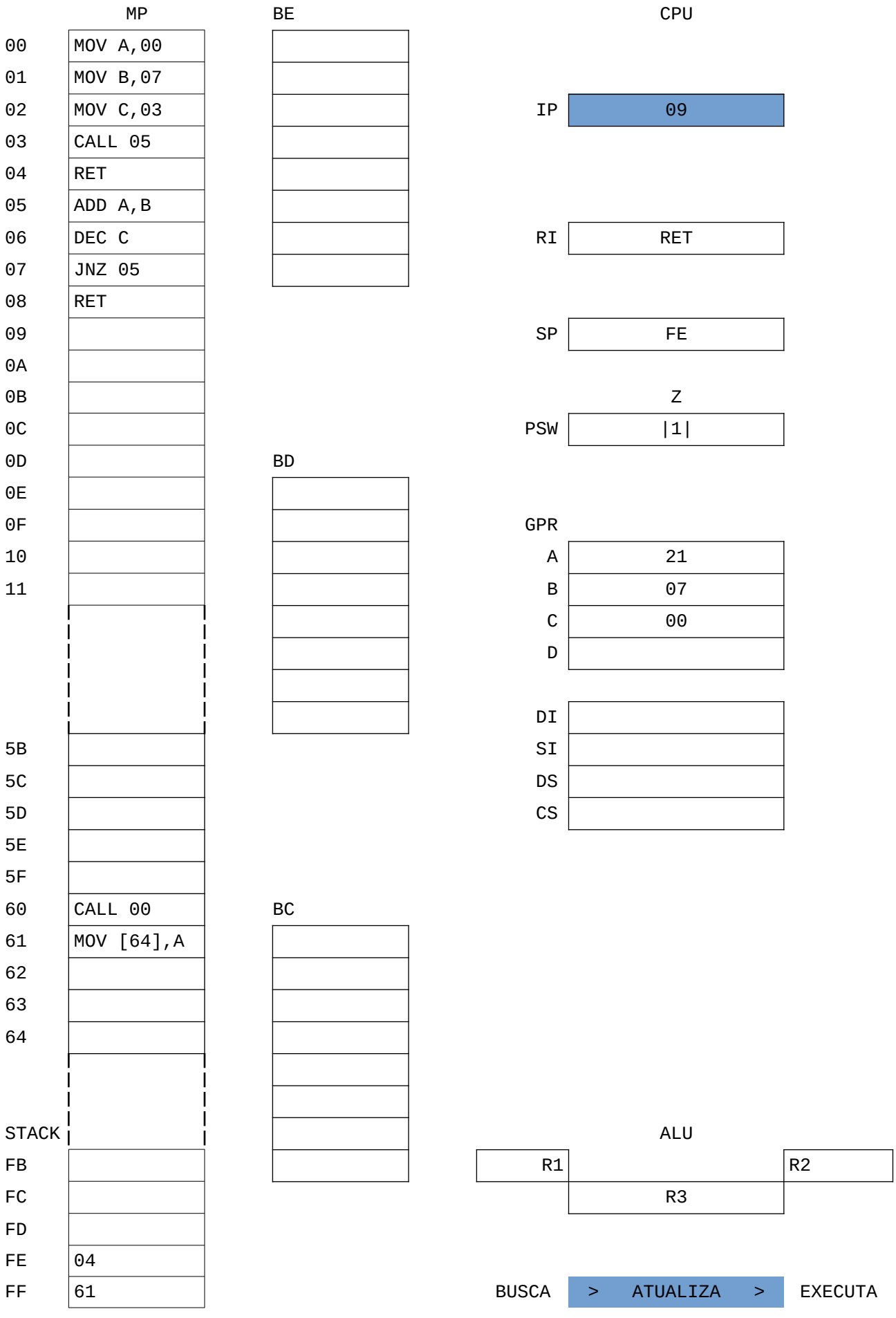
DS
CS
BC (READ)
ALU
R1
R2
R3
BUSCA > ATUALIZA > EXECUTA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FC
FD
FE 04
FF 61
BD
BC
IP 09
RI RET
SP FE
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PSW |1|
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R2
R3
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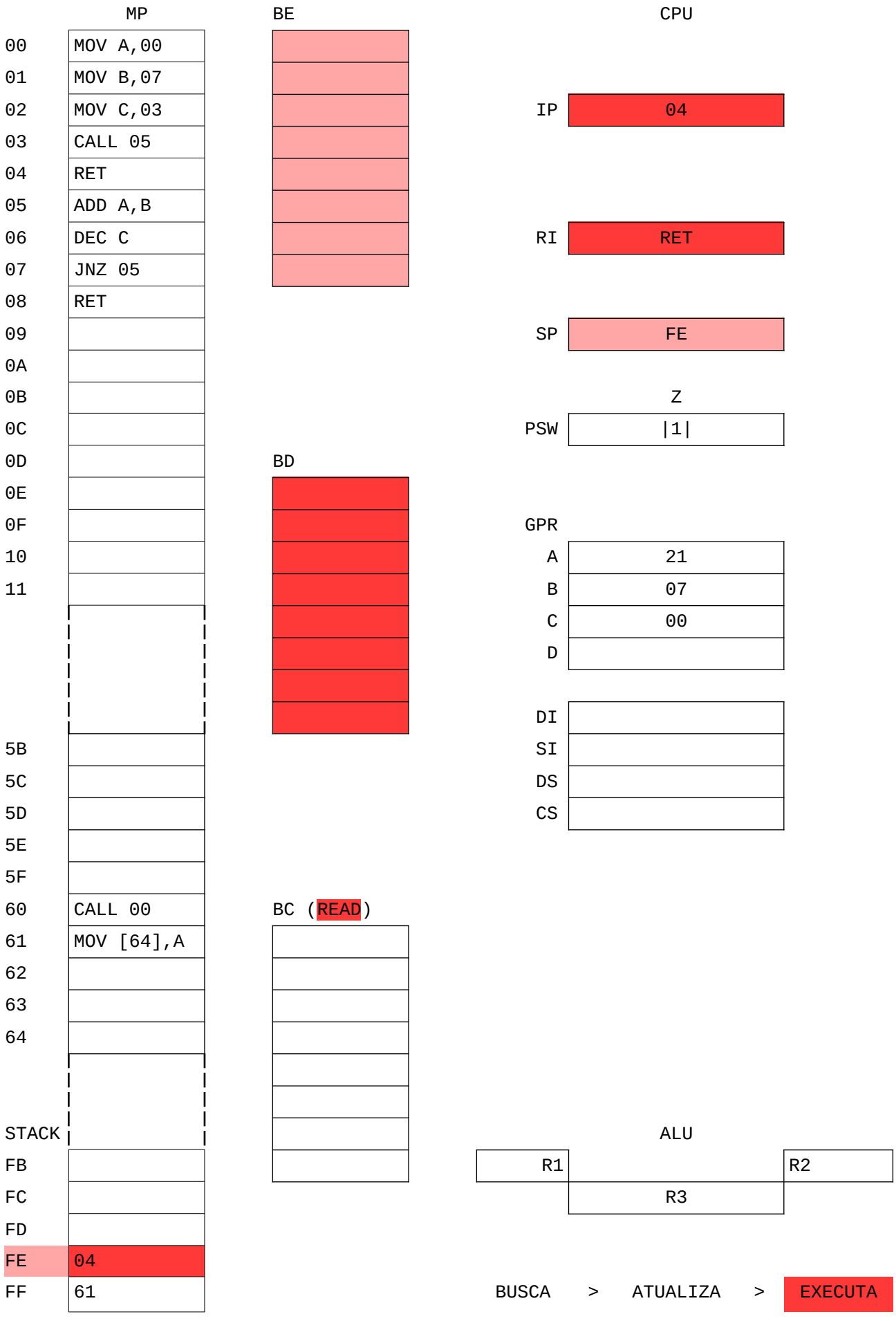
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FC
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FE 04
FF 61
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BD
BC (READ)
CPU
IP 04
RI RET
SP FE
Z
PSW |1|
GPR
A 21
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DS
CS
ALU
R1
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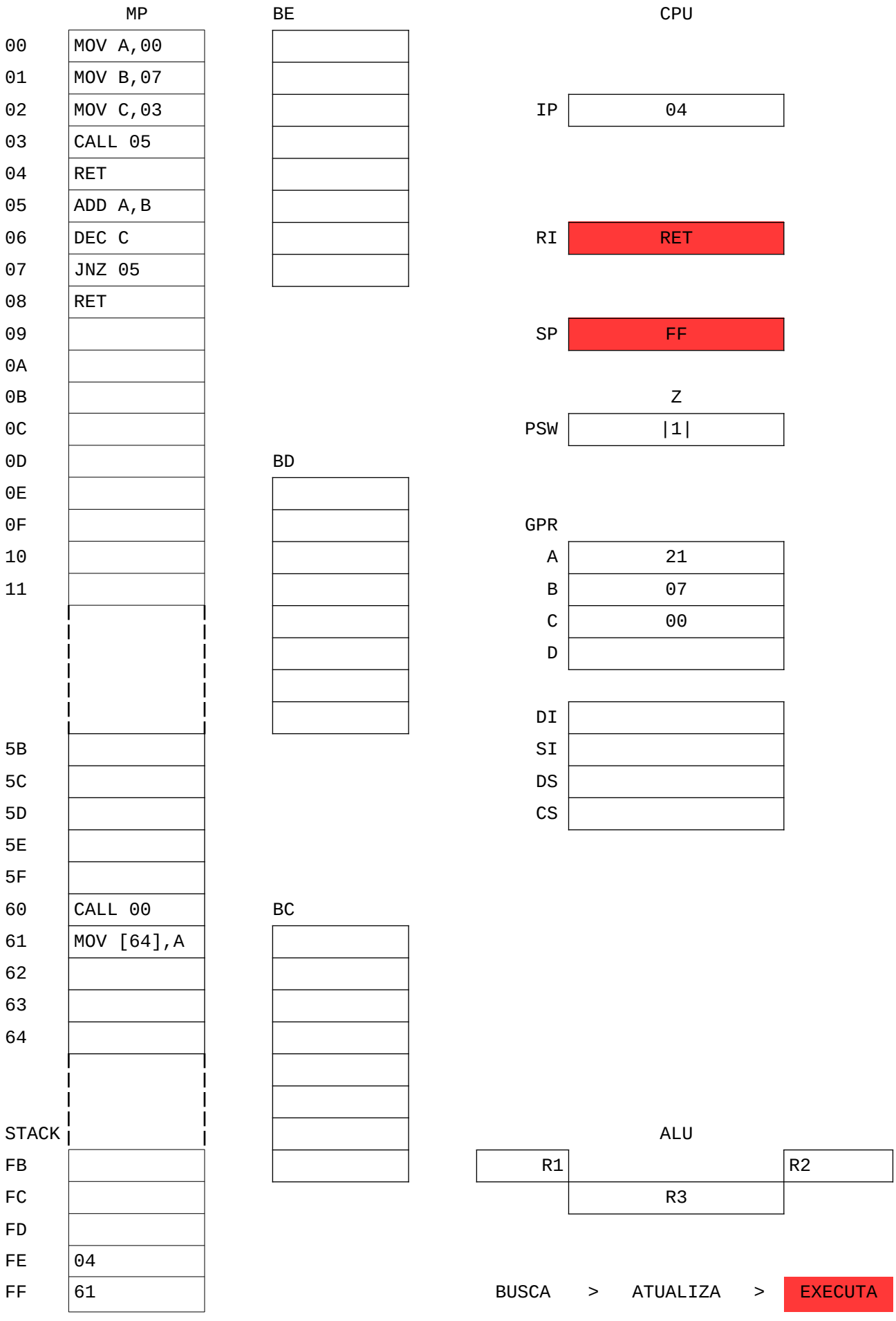
R3
BUSCA > ATUALIZA > EXECUTA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62
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STACK
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FC
FD
FE 04
FF 61
IP 04
RI RET
SP FF
Z
PSW |1|
BD
GPR
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B 07
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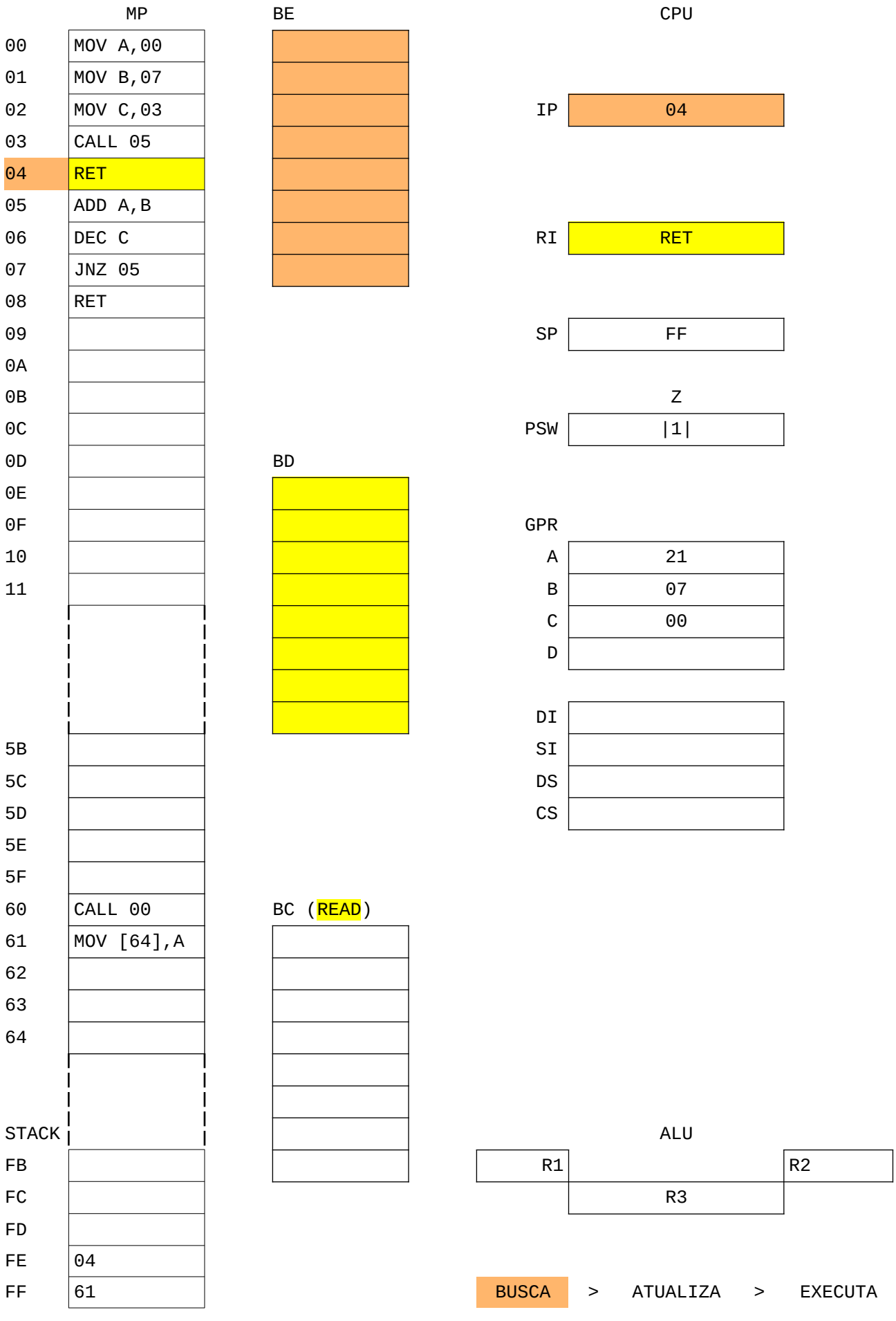
R3
BUSCA > ATUALIZA > EXECUTA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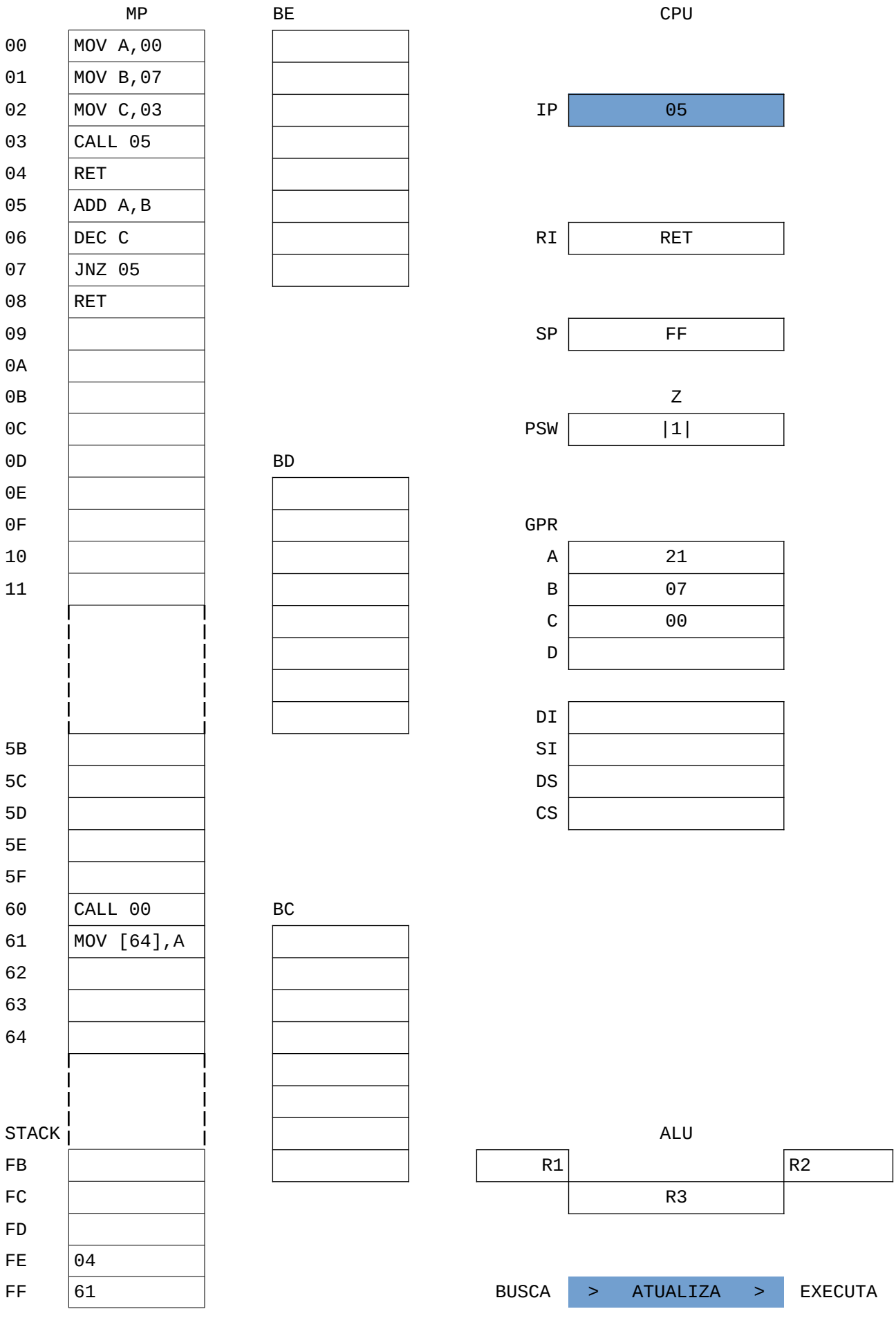
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RI RET
SP FF
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PSW |1|
GPR
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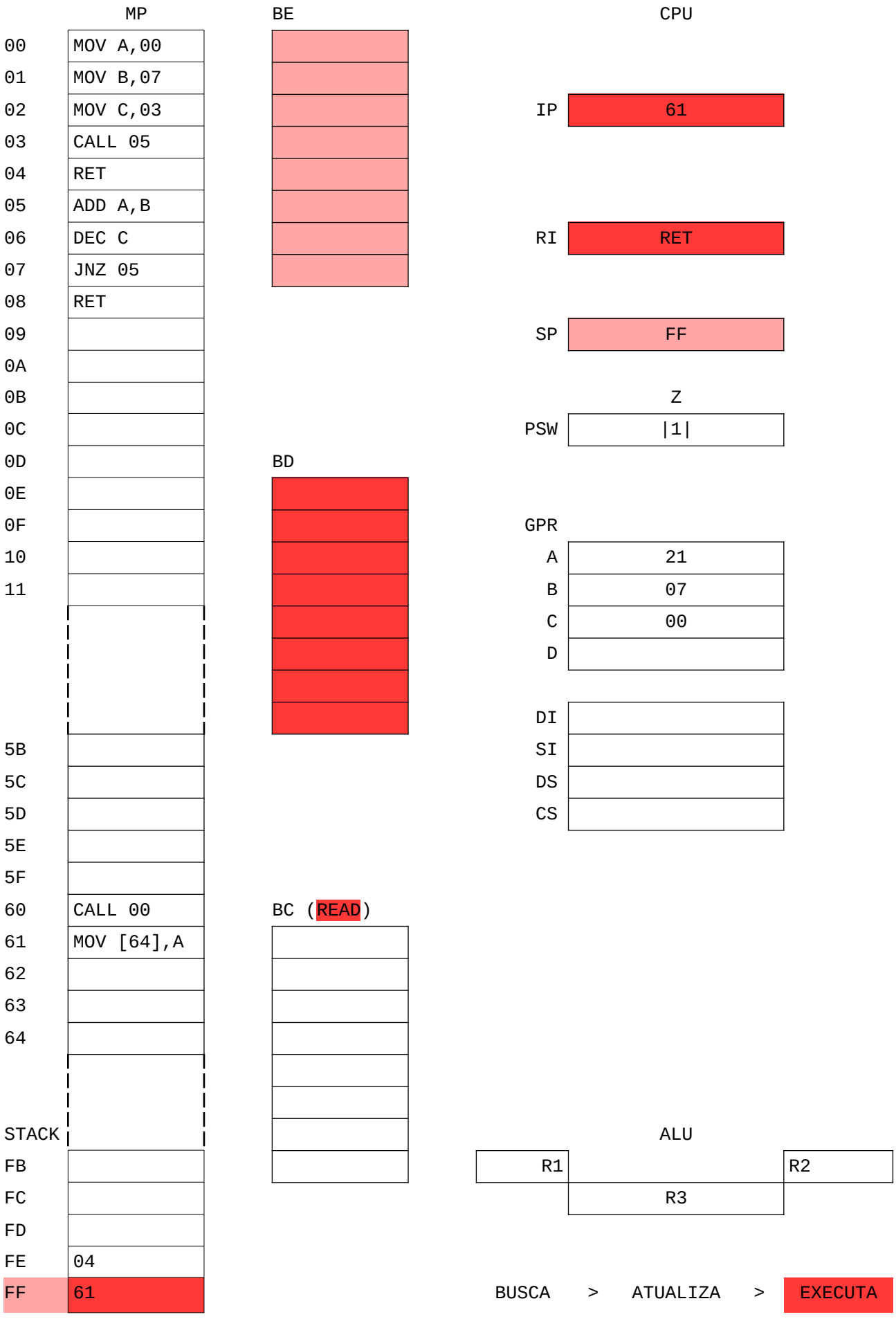
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BC (READ)
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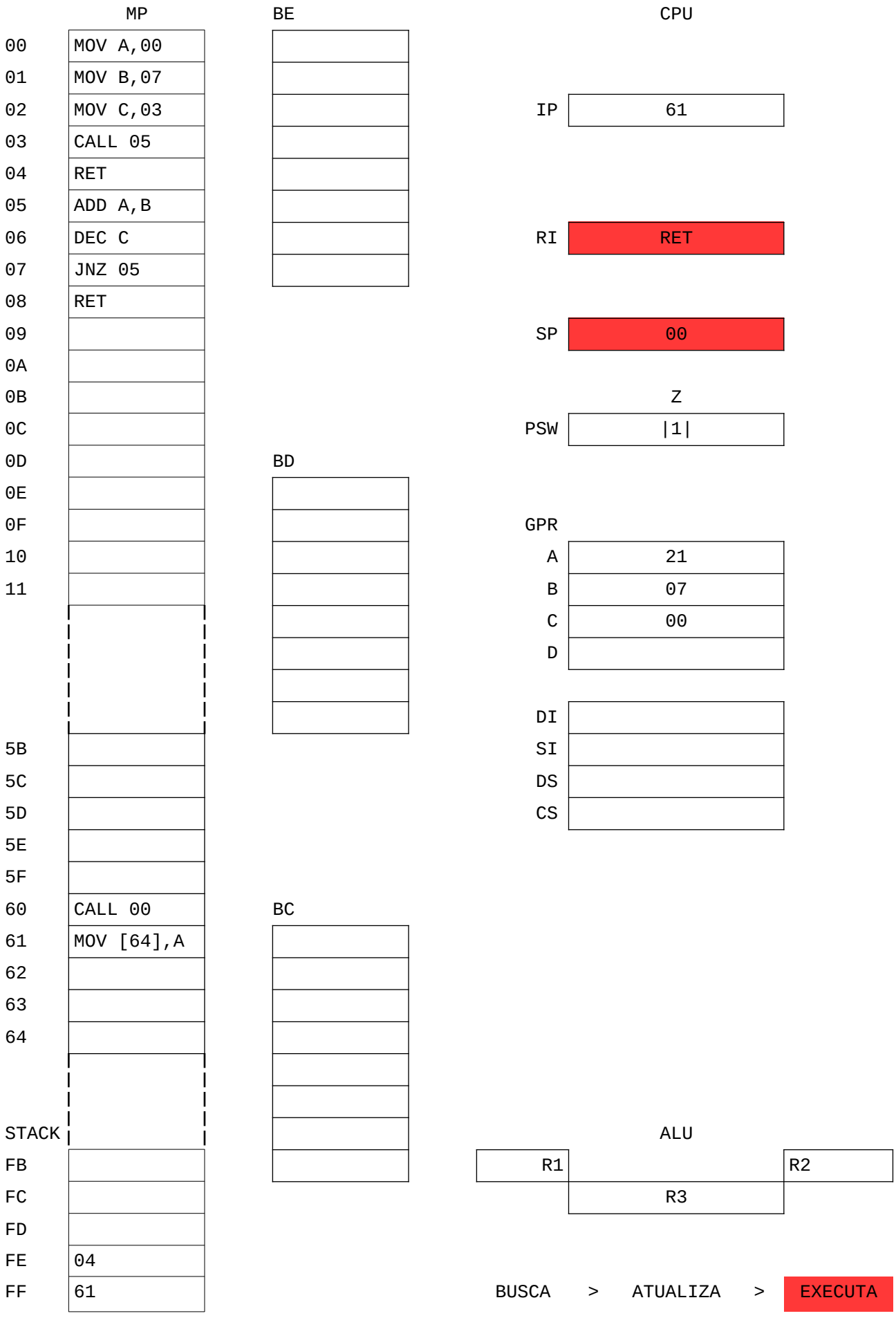
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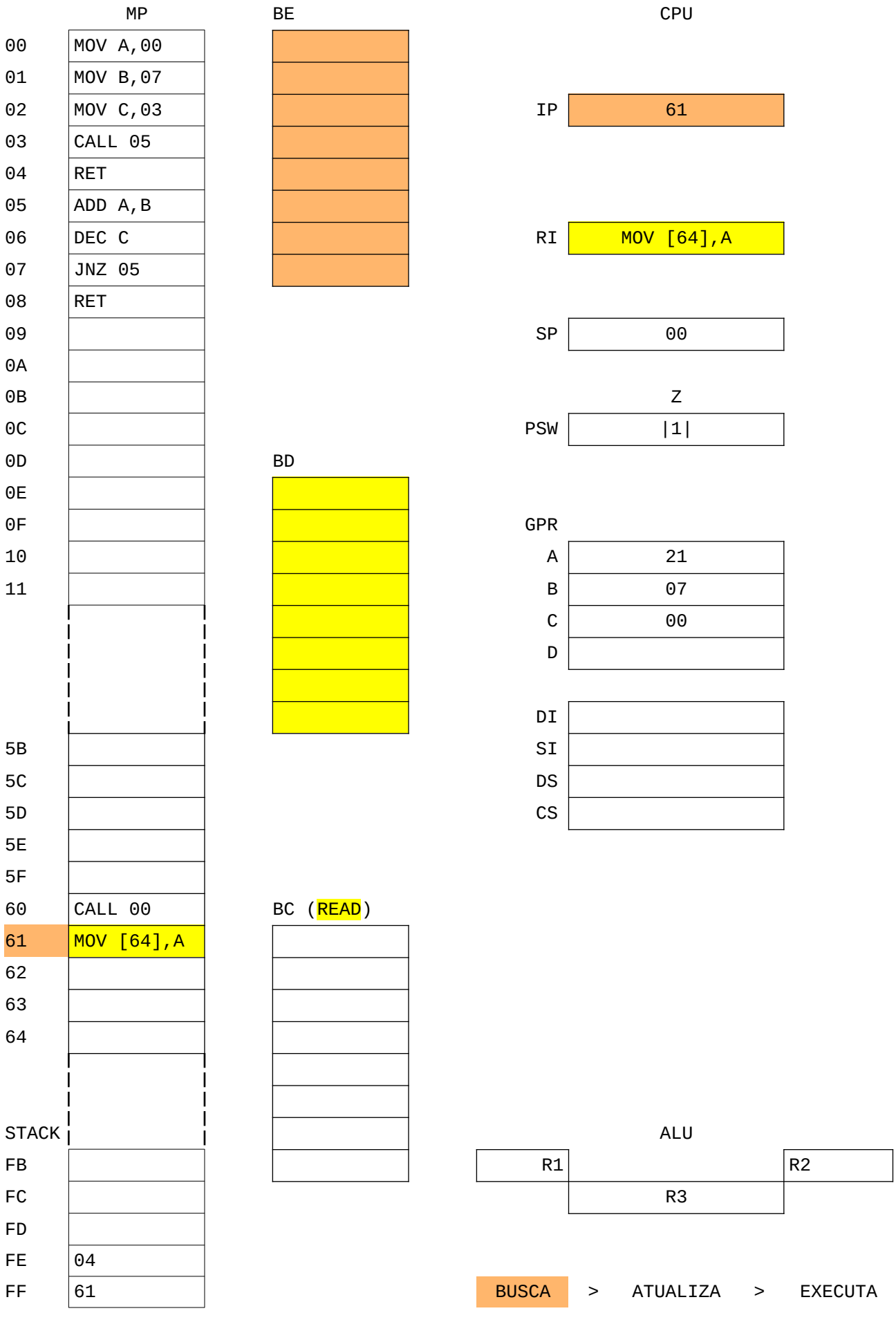
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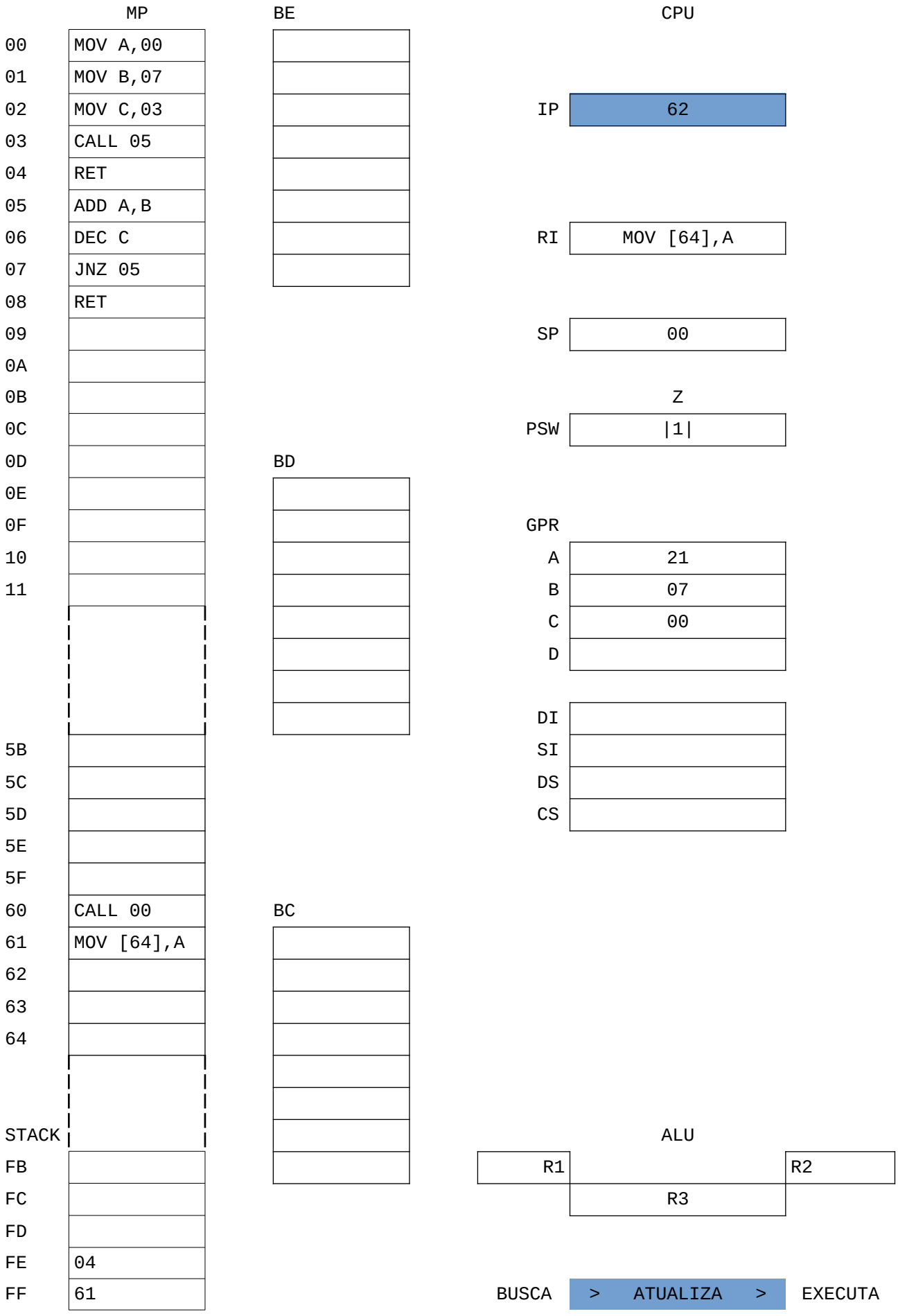
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STACK
FB
FC
FD
FE 04
FF 61
IP 62
RI MOV [64],A
SP 00
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PSW |1|
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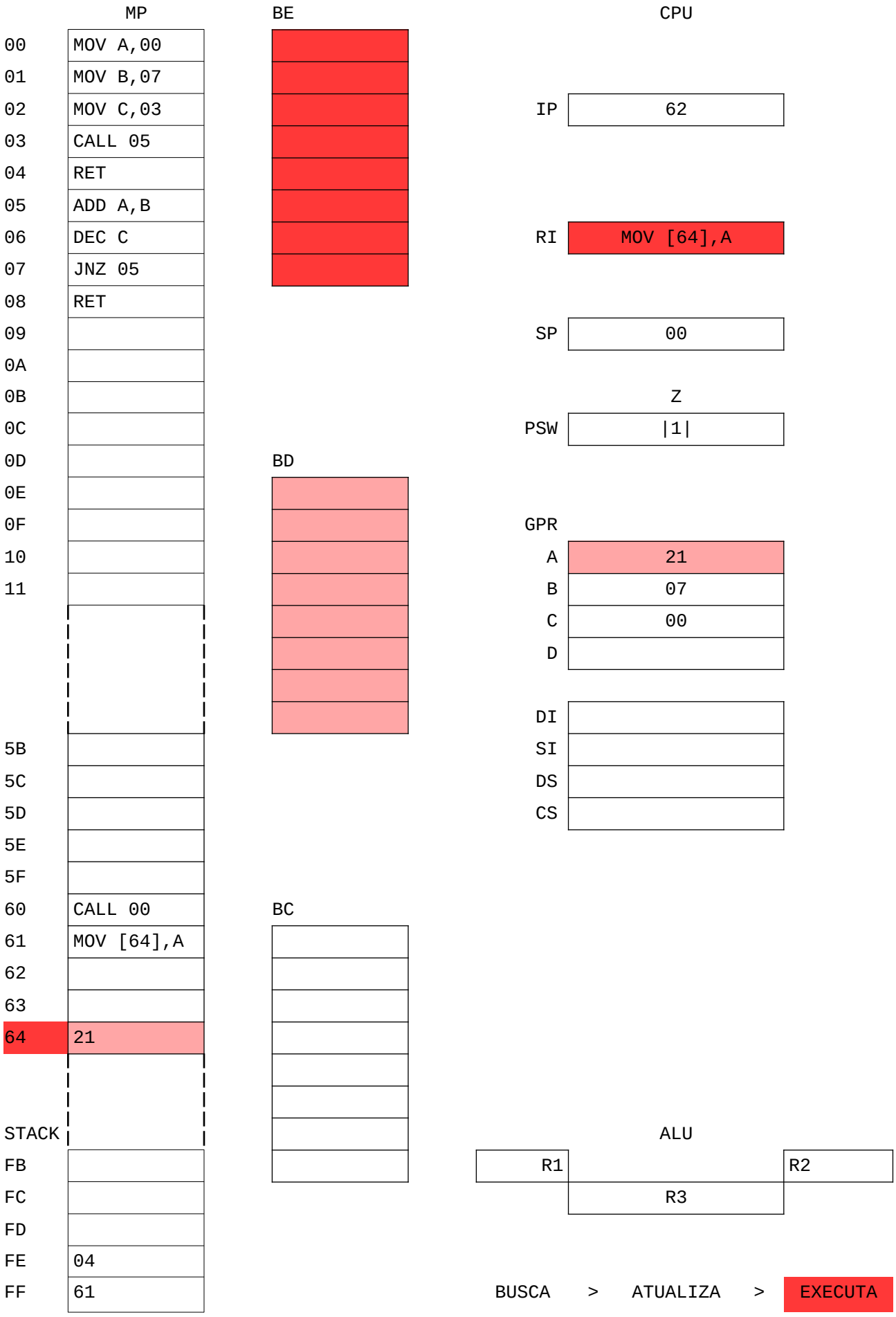
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RI MOV [64],A
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